MELSEC QnA Series

Programmable Controller

User's Manual
(Hardware)

Q2ASCPU, Q2ASCPU-S1
Q2ASHCPU, Q2ASHCPU-S1

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1. OVERVIEW

This manual explains the performance specifications, names and settings of each part, and the error codes for the Q2ASCPU, Q2ASCPU-S1, Q2ASHCPU and Q2ASHCPU-S1 (hereinafter, Q2ASCPU).
Refer to the Type A1SC24-R2/A1SH/A2SHCPU(S1)/A2ASCPU(S1/S30) User's Manual (Hardware) IB-66468 enclosed with the base unit for details on the Q2ASCPU safety precautions, general specifications, mounting and installation methods, EMC Directives, Low-voltage Directives and the input/output module specifications and connection methods.
When using the Q2ASCPU with the A1S38HB/A1S38HBEU, refer to the Q2AS(H)CPU (S1) User's Manual (Hardware) enclosed with the base unit. (All of the contents of this manual are given in the User's Manual.)

2. PERFORMANCE SPECIFICATION

2.1 Q2ASCPU Module Performance Specification

Performance specification of Q2ASCPU module is as follows:

Item		Model				Remark
		Q2ASCPU	Q2ASCPU-S1	Q2ASHCPU	Q2ASHCPU-S1	
Control method		Repetitive operation of stored program				
I/O control method		Refresh mode				I/O enabled by specifying direct I/O (DX□, DY□)
Programming language		Sequence control dedicated language				
		Relay symbol language, logic symbolic language, MELSAP3 (SFC)				
Processing speed (sequence instructions) (us/step)	LD	0.2		0.075		
	MOV	0.6		0.225		
Number of instructions (type)	Sequence instructions	39				
	Basic instructions	230				
	Application instructions	321				
	Special dedicated instructions	171				
Constant scan (ms) (Function that makes scan time constant)		5 to 2000 (configurable in multiple of 5 ms module)				Set parameter values to specify
Memory capacity		Capacity of loading memory cards (2036 kbyte maximum)				
Program capacity	Number of steps (steps)	28 k maximum	60 k maximum	28 k maximum	60 k maximum	
	Number of files (files)	28	60	28	60	
I/O device points (points)		8192 (X/Y0 to 1FFF)				Number of usable points in program
I/O points (points)		512 (X/Y0 to 1FF)	1024 (X/Y0 to 3FF)	512 (X/Y0 to 1FF)	1024 (X/Y0 to 3FF)	Number of points accessible to actual I/O modules
Clock function		Year, month, date, hour, minute, second, day of week (auto-detects leap years) Accuracy : -1.7 to +4.9s (TYP. +1.7s) / d at 0 depress centigrade Accuracy : -1.0 to +5.2s (TYP. +2.2s) / d at 25 depress centigrade Accuracy : -7.3 to +2.5s (TYP. -1.9s) / d at 55 depress centigrade				
Allowable momentary power failure period		By power supply module				
5 VDC Internal current consumption (A)		0.3	0.3	0.7	0.7	
Mass kg		0.5	0.5	0.5	0.5	
External dimension (mm (inch))		130(H) × 54.5(W) × 110(D) (5.12 × 2.15 × 4.33)				

3. PART NAMES AND SETTINGS

3.1 Part names and Settings

This section describes the name and setting of each part of the module.

Q2ASCPU, Q2ASCPU-S1, Q2ASHCPU, Q2ASHCPU-S1

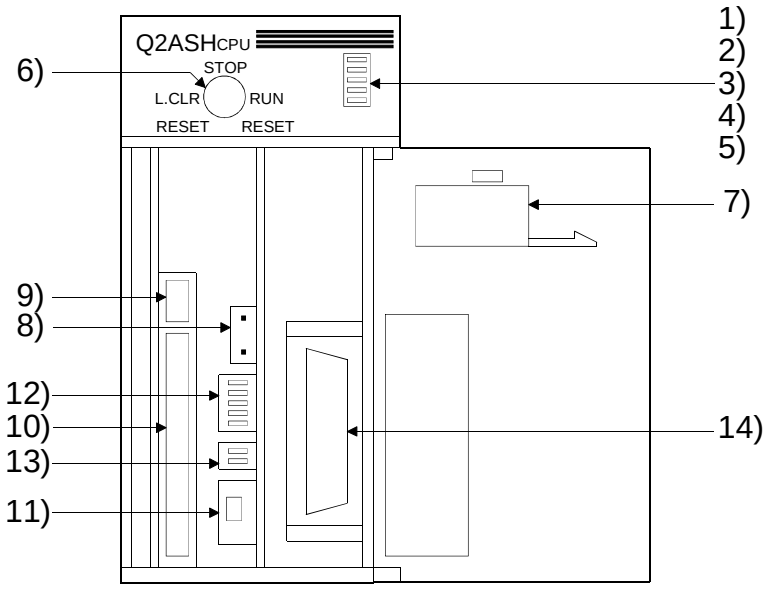
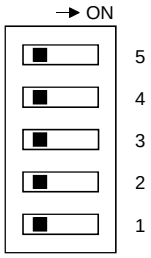


Illustration of the module with the front cover open

No.	Name	Application
1)	RUN LED	This LED indicates the CPU module operating condition. Lit :Operating with the RUN/STOP key switch set to RUN or STEP-RUN. Off :Stopped with the RUN/STOP key switch set to STOP, PAUSE, or STEP-RUN. Or, the CPU module has detected the error that would cause the operation to stop. Flash :The RUN/STOP key switch has been set from STOP to RUN after the program was written in stop mode. The CPU module is not in RUN mode. To engage the CPU module in RUN mode, set the RUN/STOP key switch to RUN, STOP, the RUN. Alternatively, reset the module using the key switch.
2)	ERROR LED	Lit :A self-diagnostic error (other than a battery error) that will not stop operation has been detected. (The parameter has been set to Continue operation at error detection.) Off :Normal Flash :An error that will stop the operation has been detected.
3)	USER LED	Lit :A error has been detected by the CHK instruction, or annunciator F has been turned ON. Off :Normal Flash :The latch clear operation has been executed.
4)	BAT. ALARM LED	Lit :Battery error has occurred due to a drop in the CPU module main unit/memory card battery voltage. Off :Normal
5)	BOOT LED	Lit :The boot operation has been completed. Off :The boot operation has not been executed.

<table>
<tr><th>No.</th><th>Name</th><th>Application</th></tr>
<tr><td>6)</td><td>RUN/STOP key switch</td><td>RUN/STOP :Executes/stops the operation of the sequence program.
L.CLR :Sets the entire data of the latch area specified by the parameter to OFF or 0. Clears the entry of the sampling trace and the status latch.
RESET :Executes the hardware reset operation and the reset at an operation error occurrence, and initializes the operation.</td></tr>
<tr><td>7)</td><td>Battery (A6BAT)</td><td>Backup battery to be used for the internal RAM and the power failure compensation function.</td></tr>
<tr><td>8)</td><td>Battery connector pin</td><td>Used to connect the battery lead wire. (The lead wire is removed from the connector at shipment in order to prevent battery consumption.)</td></tr>
<tr><td>9)</td><td>Memory card EJECT button</td><td>Used to eject the memory card from the CPU module.</td></tr>
<tr><td>10)</td><td>Memory card loading connector</td><td>This connector is used to load the memory card in the CPU module.</td></tr>
<tr><td>11)</td><td>Memory card Load/eject switch (LED equipped)
 </td><td>This switch setting determines whether or not you can load/eject the memory card during energizing. The factory default setting is OFF.
ON :Loading is prohibited. (LED is lit.)
OFF :Loading is allowed. (LED is turned off.)</td></tr>
<tr><td rowspan="5">12)</td><td rowspan="5">System setting switches 1
  </td><td>These switches allow you to set the items for the CPU module operation. The factory default setting of all switches is OFF.</td></tr>
<tr><td>SW5 :Boot setting. This switch allows you to select the memory for operation.
ON :Boot operation
OFF :Boot operation is not performed</td></tr>
<tr><td>SW2 to 4 :Parameter area. These switches allow you to select the memory into which to write the parameters.</td></tr>
<tr><td><table>
<tr><th rowspan="2"></th><th rowspan="2">Internal RAM</th><th colspan="2">Memory card</th><th rowspan="2">SW2 to 4 are valid if SW1 is OFF.</th></tr>
<tr><th>RAM</th><th>ROM</th></tr>
<tr><td>SW4</td><td>OFF</td><td>ON</td><td>OFF</td></tr>
<tr><td>SW3</td><td>OFF</td><td>OFF</td><td>ON</td></tr>
<tr><td>SW2</td><td>OFF</td><td>OFF</td><td>OFF</td></tr>
</table></td></tr>
<tr><td>SW1 :System protect. Writing to the CPU module and issuing the control instructions are prohibited.
ON :System protect is valid.
OFF :System protect is invalid.</td></tr>
<tr><td rowspan="3">13)</td><td rowspan="3">System setting switches 2
 </td><td>These switches allow you to set the items for CPU module operation. The factory default setting of all switches is OFF.</td></tr>
<tr><td>SW2 :Unused (Fixed to OFF)</td></tr>
<tr><td>SW1 :Peripheral protocol. This switch allows you to select the type of peripheral devices that are connected to the CPU modules peripheral interface.
(Set this switch to ON when you wish to access another stations ACPU from the ACPU peripheral device. The setting becomes effective as soon as you set the switch.)
ON :Peripheral device for the ACPU
OFF :Peripheral device for the Q2ASCPU</td></tr>
<tr><td>14)</td><td>RS-422 connector</td><td>Used to connect a peripheral device.</td></tr>
</table>

3.2 Relationship between switch operation and the LED indication

(1) Writing a program while the CPU module is stopped:

Follow the procedure below to write a program while the CPU module is stopped:

(a) RUN/STOP key switch: STOP
RUN LED: Off ..... CPU module is in STOP mode. → Write a program.

(b) RUN/STOP key switch: RESET
RUN LED: Off ..... CPU module is in STOP mode.

(c) RUN/STOP key switch: STOP → RUN
RUN LED: Lit ..... CPU module is in RUN mode.

POINT
• After writing a program (except for online program write), perform reset operation, and then place the CPU module in the RUN status. • When remote STOP is switched to RUN, the CPU module is not put in the "PROG CHECK" status but is placed in the RUN status.

(2) Latch clear operation:

Operate the RUN/STOP key switch as follows to execute the latch clear operation:

(a) Turn the RUN/STOP key switch of the CPU module from the "STOP" position to the "L. CLR" position several times to flicker the "USER LED" on the CPU module front.
Normally, the LED flickers when the switch is turned several times (three or four times).
When the "USER LED" flickers, it indicates that latch clear is ready.

(b) After the "USER LED" has flickered, turning the RUN/STOP key switch from the "STOP" position to the "L. CLR" position again executes latch clear and lights up the "USER LED".
If the "USER LED" comes on for two seconds and then goes off, it indicates that latch clear is completed normally.

(c) To cancel latch clear midway, turn the RUN/STOP key switch to the "RUN" position to place the CPU module in the RUN state, or turn it to the "RESET" position to make a reset.

POINT
• You can make latch clear valid or invalid for each device via the device setting in parameter mode. • Instead of using the RUN/STOP key switch, you can also execute the latch clear operation remotely from the peripheral GPP function device. (Refer to the Q2AS(H)CPU(S1) User's Manual (Detailed Manual).)

(3) Removing the memory card while the PLC power is on:
Operate the memory card load/eject switch as described below the memory card while the PLC power is still on:
(a) Load/eject switch: ON,
Load/eject switch internal LED:Lit.....Ejecting the memory card is prohibited.
(b) Load/eject switch: OFF,
Load/eject switch internal LED:Off.....Ejecting the memory card is allowed.
.....→ Remove the memory card.

POINT

- The load/eject switch internal LED may not be turned off when you are using the memory card for the CPU module system function (such as sampling trace and status latch) or for the program.
  In this case, quit the corresponding system function or program that is using the memory card. Then, make sure that the load/eject switch internal LED is turned off, and remove the memory card.
- Do not turn on the memory card load/eject switch after you have removed the memory card. Otherwise, on error will occur.
- When there are parameter-set file registers, local devices or failure history, the memory card cannot be the removed. If the "memory card in/out" switch is turned OFF, the in/out switch built-in LED does not go off. For the file registers, the memory card can be removed when they are set to be unused with the QDRSET(P) instruction.

(4) Loading the memory card while the PLC power is on:
Operate the memory card load/eject switch as described below to load the memory card while the PLC power is still on:
(a) Load the memory card.
(b) Load/eject switch: ON,
Load/eject switch internal LED: Lit.....Ejecting the memory card is prohibited.

POINT

- Be sure to turn on the memory card load/eject switch after you have loaded the memory card. Otherwise, you will not be able to use the card.
- Since mount processing is performed again after the memory card is inserted, note that the scan time of one scan when mount processing is performed increases by a maximum of 10ms.

4. ABOUT FAIL-SAFE CIRCUITS

4.1 About Fail-safe Circuits

When the PLC is powered on and off, normal control output may not be done momentarily due to a delay or a start-up time difference between the PLC power supply and the external power supply (DC in particular) for the control target.

For example, if the external power supply for the controlled object is switched on in a DC output module and then the PLC power supply is switched on, the DC output module may provide false output instantaneously at power-on of the PLC. Therefore, it is necessary to make up a circuit that can switch on the PLC power supply first.

Also, an abnormal operation may be performed if an external power supply fault or PLC failure takes place.

To prevent any of these abnormal operations from leading to the abnormal operation of the whole system and in a fail-safe viewpoint, areas which can result in machine breakdown and accidents due to abnormal operations (e.g. emergency stop, protective and interlock circuits) should be constructed outside the PLC.

The next page shows an example of the system design circuits, based on the considerations described above.

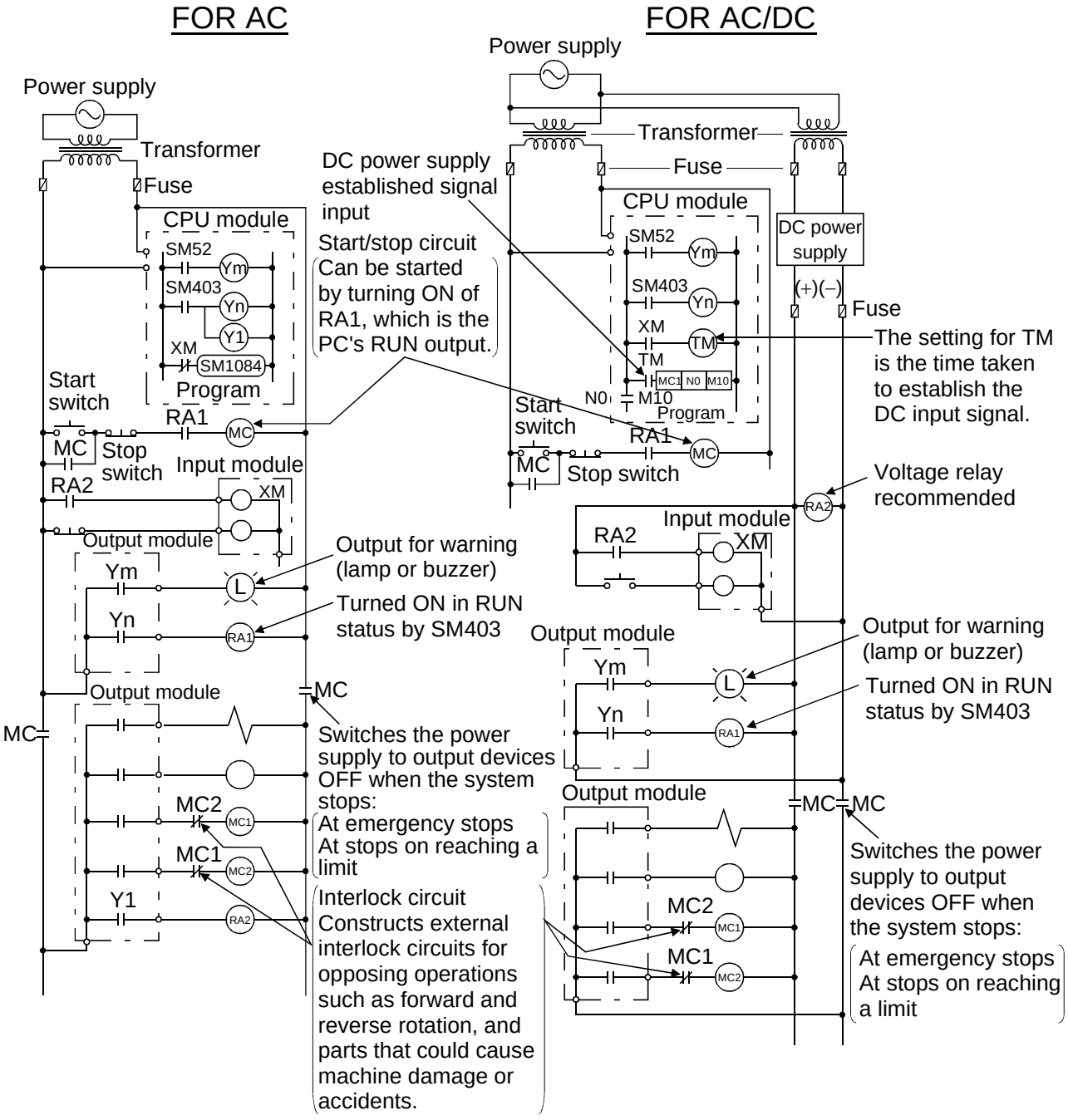
⚠DANGER

- Install a safety circuit external to the PLC that keeps the entire system safe even when there are problems with the external power supply or the PLC module. Otherwise, trouble could result from erroneous output or erroneous operation.
  (1) Outside the PLC, construct mechanical damage preventing interlock circuits such as emergency stop, protective circuits, positioning upper and lower limits switches and interlocking forward/reverse operations.
  (2) When the PLC detects the following problems, it will stop calculation and turn off all output in the case of (a). In the case of (b), it will stop calculation and hold or turn off all output according to the parameter setting. Note that the AnS series module will turn off the output in either of cases (a) and (b).
    (a) The power supply module has over current protection equipment and over voltage protection equipment.
    (b) The CPU module self-diagnostic functions, such as the watchdog timer error, detect problems. In addition, all output will be turned on when there are problems that the CPU module cannot detect, such as in the I/O controller. Build a fail safe circuit exterior to the PLC that will make sure the equipment operates safely at such times.
  (3) Output could be left on or off when there is trouble in the outputs module relay or transistor. So build an external monitoring circuit that will monitor any single outputs that could cause serious trouble.
- Build a circuit that turns on the external power supply when the PLC main module power is turned on. If the external power supply is turned on first, it could result in erroneous output or erroneous operation.
- When there are comminication problems with the data link, refer to the corresponding data link manual for the operating status of each station. Not doing so could result in erroneous output or erroneous operation. operating status of each station. Not doing so could result in erroneous output or erroneous operation.

⚠DANGER	● When connecting a peripheral device to a CPU module, or a PC to an intelligent function module to control a PLC in operation (data change), configure an interlock circuit on the sequence program so that the entire system operates safely at any time. Also before exercising other control (program change, operating status change (status control)) on the running PLC, read the manual carefully and fully confirm safety. Especially for the above control on the remote PLC from an external device, an immediate action may not be taken for PLC trouble due to a data communication fault. In addition to configuring up the interlock circuit in the sequence program, corrective and other actions to be taken as a system for the occurrence of a data communication fault should be predetermined between the external device and PLC CPU.

⚠CAUTION	● Do not bunch the control wires or communication cables with the main circuit or power wires, or install them close to each other. They should be installed 100 mm (3.94 inch) or more from each other. Not doing so could result in noise that would cause erroneous operation. ● When controlling items like lamp load, heater or solenoid valve using an output module, large current (approximately ten times greater than that present in normal circumstances) may flow when the output is turned OFF to ON. Take measures such as replacing the module with one having sufficient rated current.

(1) Examples of system design circuits



Follow the procedure below to start up the power supply:

For AC

1) Turn the power ON.
2) Set the CPU module RUN.
3) Set the start switch to ON.
4) Turn ON the magnetic contactor (MC) and drive the output device using the program.

For AC/DC

1) Turn the power ON.
2) Select the CPU module RUN.
3) When the DC power is established, turn RA2 ON.
4) When 100% of the DC power is established, set the timer (TM) to ON.
   (The TM value should be the time required from when RA2 is turned ON until 100% of the DC voltage is established. Set the value to 0.5 seconds.)
5) Set the start switch to ON.
6) Turn ON the magnetic contactor (MC), and drive the output equipment using the program. (When you are using a voltage relay for RA2, the timer (TM) on the program is not required.)

(2) Fail-safe action during PC malfunction

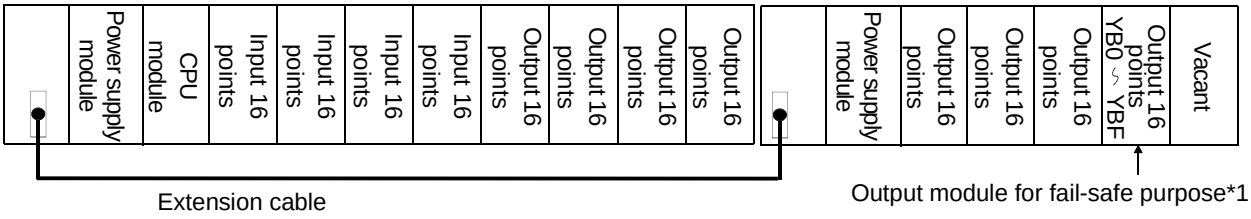
Although a malfunction of the PCs CPU module and memory can be usually detected by the self-diagnostic function, the CPU module sometimes cannot detect a malfunction if the I/O controller is faulty.

In this case, depending on the degree of the malfunction, all devices might turn on or off, or you might have a situation in which you cannot maintain normal operation or safety of the controlled item.

We have done our best to maintain the highest quality as a manufacturer. However, should the PC become defective for some reason, configure an external fail-safe circuit to prevent mechanical damages and possible accidents.

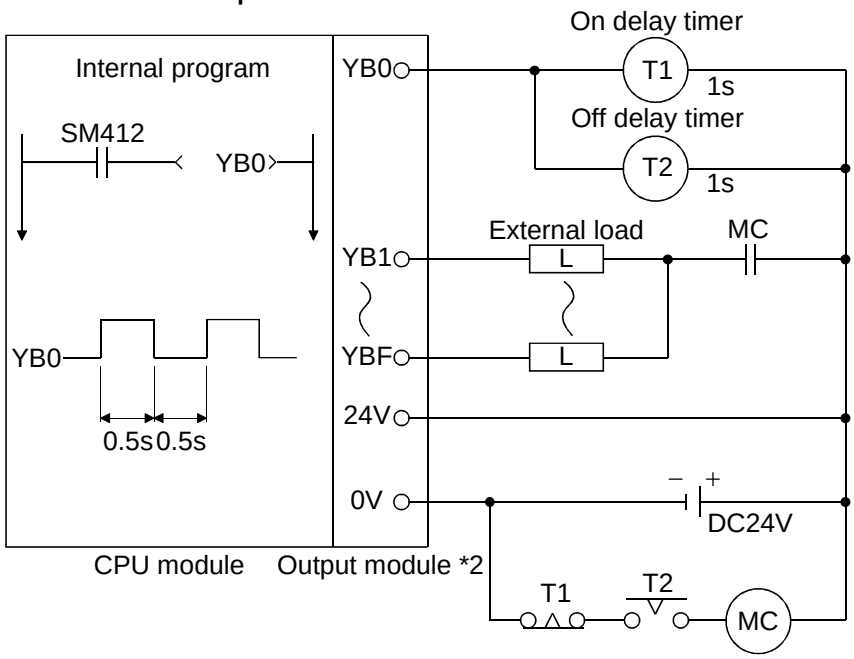
Examples of a system and its fail-safe circuitry are described below:

<System example>



*1: The output moduel for fail-safe purpose should be loaded in the last slot of the system. (YB0 to YBF in the above system.)

<Fail-safe circuit example>



*2: YB0 will repeats turning ON and OFF with an interval of 0.5 seconds . Use a contactless output module (the example above uses a transistor.).

MEMO

5. ERROR CODES

The Q2ASCPU uses the self-diagnostic function to display an error code (LED indicator) and store the error information in the special relay SM and the special register SD, if an error occurs when the power is turned on to the PC or when the PC starts or running.
This chapter explains the details of errors that could occur on the Q2ASCPU and how to take a corrective action against them.

REMARK

The error code of the error that occurred when a general data processing request is made from the peripheral device, special function module or network system is not stored into SD0 of the Q2ASCPU.
The error code is returned to the source of the general data processing request.

5.1 Reading Error Codes

When an error occurs, the error code or error message, etc., can be read out at GPP function. For details regarding the GPP function operation procedure, refer to the GX Developer OPERATING MANUAL or SW□IVD-GPPQ OPERATING MANUAL.

5.2 Error Code List

The following information deals with error codes and the meanings, causes, and corrective measures of error messages.

<Relevant CPU>

○ : Indicates all the QnACPUs and QCPU.
QCPU : Indicates all the Q series CPU modules.
Q00J/Q00/Q01 : Indicates the Basic model QCPU.
Qn(H) : Indicates the High Performance model QCPU.
QnPH : Indicates the Process CPU.
QnPRH : Indicates the Redundant CPU.
QnA : Indicates the QnA series and Q2ASCPU series.
Rem : Indicates the MELSECNET/H remote I/O modules.
Each CPU module model name: Indicates the relevant specific CPU module.
(Example: Q4AR, Q2AS)

Error Code (SD0)*1	Error Messages	Common Information (SD5 to 15)	Individual Information (SD16 to 26)	LED Status		Operating Statuses of CPU	Diagnostic Timing	
				RUN	ERROR			
1000	MAIN CPU DOWN	----	----	Off	Flicker	Stop	Always	
1001								
1002								
1003								
1004								
1005								
1006								
1007								
1008								
1009								

*1 : Characters in parentheses ( ) indicate the special register numbers where individual information is being stored.

	Error Code (SD0)*1	Error Contents and Cause	Corrective Action	Corresponding CPU
	1000	Run mode suspended or failure of main CPU • Malfunctioning due to noise or other reason • Hardware fault	• Take noise reduction measures. • Reset the CPU module and RUN it again.If the same error is displayed again, this suggests a CPU module hardware fault.(Contact your local Mitsubishi representative.)	○
	1001			Q00J/Q00/Q01 Qn(H) QnPH QnPRH
	1002			
	1003			
	1004			
	1005			Q00J/Q00/Q01 Qn(H) QnPH QnPRH Rem
	1006			Q00J/Q00/Q01 Qn(H) QnPH QnPRH
	1007			Qn(H) QnPH QnPRH
	1008			
	1009	• An error is detected on the power supply module, CPU module, base unit or extension cable. • The redundant power supply module failure is detected in both systems.	Reset the CPU module and RUN it again.If the same error is displayed again, the power supply module, CPU module, main base unit, extension base unit or extension cable is faulty. (Contact your local Mitsubishi representative.)	Q00J/Q00/Q01*12 Qn(H)*14 QnPH QnPRH

*1 : Characters in parentheses ( ) indicate the special register numbers where individual information is being stored.

*12 : This applies to the CPU module of function version B or later.

*14 : This applies to the CPU module with serial No. of first 5 digits "04101" or later.

Error Code (SD0)*1	Error Messages	Common Information (SD5 to 15)	Individual Information (SD16 to 26)	LED Status		Operating Statuses of CPU	Diagnostic Timing	
				RUN	ERROR			
1010 1011 1012	END NOT EXECUTE	----	----	Off	Flicker	Stop	When an END instruction executed.	
1020	SFCP. END ERROR	----	----	Off	Flicker	Stop	When SFC program is executed	

*1 : Characters in parentheses ( ) indicate the special register numbers where individual information is being stored.

	Error Code (SD0)*1	Error Contents and Cause	Corrective Action	Corresponding CPU
	1010 1011 1012	Entire program was executed without the execution of an END instruction. • When the END instruction is executed it is read as another instruction code, e.g. due to noise. • The END instruction has been changed to another instruction code somehow.	• Take noise reduction measures. • Reset the CPU module and RUN it again. If the same error is displayed again, this suggests a CPU module hardware fault. (Contact your local Mitsubishi representative.)	○
	1020	The SFC program cannot be normally terminated due to noise or other reason. • The SFC program cannot be normally terminated due to noise or any similar cause. • The SFC program cannot be normally terminated for any other reason.		Q00J/Q00/ Q01*12 QnPH

*1 : Characters in parentheses ( ) indicate the special register numbers where individual information is being stored.

*12 : This applies to the CPU module of function version B or later.

Error Code (SD0)*1	Error Messages	Common Information (SD5 to 15)	Individual Information (SD16 to 26)	LED Status		Operating Statuses of CPU	Diagnostic Timing	
				RUN	ERROR			
1101 1102 1103 1104 1105	RAM ERROR	----	----	Off	Flicker	Stop	At power ON/ At reset	
1106	RAM ERROR	----	----	Off	Flicker	Stop	STOP→RUN/ When an END instruction executed.	
1107 1108	RAM ERROR	----	----	Off	Flicker	Stop	At power ON/ At reset	
1110	TRK. CIR. ERROR	----	----	Off	Flicker	Stop	At power ON/ At reset	
1111		----	----	Off	Flicker	Stop		

*1 : Characters in parentheses ( ) indicate the special register numbers where individual information is being stored.

	Error Code (SD0)*1	Error Contents and Cause	Corrective Action	Corresponding CPU
	1101	The sequence program storing built-in RAM/program memory in the CPU module is faulty.	This suggests a CPU module hardware fault. (Contact your nearest Mitsubishi representative.)	○
	1102	The work area RAM in the CPU module is faulty.		
	1103	The device memory in the CPU module is faulty.		
	1104	The address RAM in the CPU module is faulty.		
	1105	The system RAM in the CPU module is faulty.		Q4AR
		The CPU memory in the CPU module is faulty.		Q00J/Q00/Q01
		The CPU shared memory in the CPU module is faulty.	• Take noise reduction measures. • Reset the CPU module and RUN it again.If the same error is displayed again, this suggests a CPU module hardware fault. (Contact your local Mitsubishi representative.)	Qn(H)*12 QnPH QnPRH
	1106	The battery is dead. The program memory in the CPU module is faulty.	• Check the battery to see if it is dead or not. If dead, replace the battery. • Take noise reduction measures. • Format the program memory, write all files to the PLC, then reset the CPU module, and RUN it again. If the same error is displayed again, the possible cause is a CPU module hardware fault. (Contact your local Mitsubishi representative, explaining a detailed description of the problem.)	QnPRH
	1107	The work area RAM in the CPU module is faulty.	This suggests a CPU module hardware fault. (Contact your local Mitsubishi representative.)	
	1108			
	1110	A fault was detected by the initial check of the tracking hardware.		
	1111	A tracking hardware fault was detected.		

*1 : Characters in parentheses ( ) indicate the special register numbers where individual information is being stored.

*12 : his applies to the CPU module of function version B or later.

Error Code (SD0)*1	Error Messages	Common Information (SD5 to 15)	Individual Information (SD16 to 26)	LED Status		Operating Statuses of CPU	Diagnostic Timing	
				RUN	ERROR			
1112 1113	TRK. CIR. ERROR	----	----	Off	Flicker	Stop	During running	
1115	TRK. CIR. ERROR	----	----	Off	Flicker	Stop	At power ON/ At reset	
1116	TRK. CIR. ERROR	----	----	Off	Flicker	Stop	During running	

*1 : Characters in parentheses ( ) indicate the special register numbers where individual information is being stored.

	Error Code (SD0)*1	Error Contents and Cause	Corrective Action	Corresponding CPU
	1112 1113	A tracking hardware fault was detected during running. • The tracking cable was disconnected and reinserted without the standby system being powered off or reset. • The tracking cable is not secured by the connector fixing screws. • The error occurred at a startup since the redundant system startup procedure was not followed.	• Start after checking that the tracking cable is connected. If the same error is displayed again, the cause is the hardware fault of the tracking cable or CPU module. (Please contact your local Mitsubishi representative, explaining a detailed description of the problem.) • Confirm the redundant system startup procedure, and execute a startup again. For details, refer to the QnPRHCPU User's Manual (Redundant System).	QnPRH
	1115	A fault was detected by the initial check of the tracking hardware.	This suggests a CPU module hardware fault. (Contact your nearest Mitsubishi representative.)	
	1116	A tracking hardware fault was detected during running. • The tracking cable was disconnected and reinserted without the standby system being powered off or reset. • The tracking cable is not secured by the connector fixing screws. • The error occurred at a startup since the redundant system startup procedure was not followed.	• Start after checking that the tracking cable is connected. If the same error is displayed again, the cause is the hardware fault of the tracking cable or CPU module. (Please contact your local Mitsubishi representative, explaining a detailed description of the problem.) • Confirm the redundant system startup procedure, and execute a startup again. For details, refer to the QnPRHCPU User's Manual (Redundant System).	

*1 : Characters in parentheses ( ) indicate the special register numbers where individual information is being stored.

Error Code (SD0)*1	Error Messages	Common Information (SD5 to 15)	Individual Information (SD16 to 26)	LED Status		Operating Statuses of CPU	Diagnostic Timing	
				RUN	ERROR			
1200	OPE. CIRCUIT ERR.	----	----	Off	Flicker	Stop	At power ON/ At reset	
1201								
1202								
1203	OPE. CIRCUIT ERR.	----	----	Off	Flicker	Stop	When an END instruction executed.	
1204								
1205								
1206	OPE. CIRCUIT ERR.	----	----	Off	Flicker	Stop	When instruction executed.	
1300	FUSE BREAK OFF	Unit No. [For Remote I/O network] Network No./ Station No.	----	Off/ On	Flicker/ On	Stop/ Continue*2	When an END instruction executed.	

*1 : Characters in parentheses ( ) indicate the special register numbers where individual information is being stored.

*2 : The CPU module operation status when an error occurs can be set at the parameters. (LED display will change accordingly.)

	Error Code (SD0)*1	Error Contents and Cause	Corrective Action	Corresponding CPU
	1200	The operation circuit for indexing in the CPU module does not operate normally.	This suggests a CPU module hardware fault. (Contact your local Mitsubishi representative.)	○
	1201	The hardware (logic) in the CPU module does not operate normally.		
	1202	The operation circuit for sequence processing in the CPU module does not operate normally.		
	1203	The operation circuit for indexing in the CPU module does not operate normally.		Q4AR QnPRH
	1204	The hardware (logic) in the CPU module does not operate normally.		
	1205	The operation circuit for sequence processing in the CPU module does not operate normally.		
	1206	The DSP operation circuit in the CPU module does not operate normally.		Q4AR
	1300	There is an output module with a blown fuse.	• Check FUSE. LED of the output modules and replace the module whose LED is lit. (The module with a blown fuse can also be identified using GX Developer. Check the special registers SD1300 to SD1331 to see if the bit corresponding to the module is "1".) • When a GOT is bus-connected to the main base unit or extension base unit, check the connection status of the extension cable and the earth status of the GOT.	Qn(H) QnPH QnPRH Rem
		There is an output module with a blown fuse.	Check ERR. LED of the output modules and replace the module whose LED is lit. (The module with a blown fuse can also be identified using GX Developer. Check the special registers SD130 to SD137 to see if the bit corresponding to the module is "1".)	Q00J/Q00/Q01

*1 : Characters in parentheses ( ) indicate the special register numbers where individual information is being stored.

Error Code (SD0)*1	Error Messages	Common Information (SD5 to 15)	Individual Information (SD16 to 26)	LED Status		Operating Statuses of CPU	Diagnostic Timing	
				RUN	ERROR			
1300	FUSE BREAK OFF	Unit No. [For Remote I/O network] Network No./ Station No.	----	Off/ On	Flicker/ On	Stop/ Continue*2	When an END instruction executed.	

*1 : Characters in parentheses ( ) indicate the special register numbers where individual information is being stored.

*2 : The CPU module operation status when an error occurs can be set at the parameters. (LED display will change accordingly.)

	Error Code (SD0)*1	Error Contents and Cause	Corrective Action	Corresponding CPU
	1300	There is an output module with a blown fuse.	• Check ERR. LED of the output modules and replace the fuse of the module whose LED is lit. • Read the common information of the error using the peripheral device and replace the fuse at the output module corresponding to the numerical value (module No.) reading. Alternatively, monitor special registers SD1300 to SD1331 with the peripheral device and change the fuse of the output module whose bit has a value of "1". • When a GOT is bus-connected to the main base unit or extension base unit, check the connection status of the extension cable and the earth status of the GOT.	QnA Q4AR
		• There is an output module with a blown fuse. • External power supply for output load is turned off or disconnected.	• Check ERR. LED of the output modules and replace the module whose LED is lit. • Read the common information of the error using the peripheral device and replace the fuse at the output module corresponding to the numerical value (module No.) reading. Alternatively, monitor special registers SD1300 to SD1331 with the peripheral device and change the fuse of the output module whose bit has a value of "1". • Check whether the external power supply for output load is ON or OFF. • When a GOT is bus-connected to the main base unit or extension base unit, check the connection status of the extension cable and the earth status of the GOT.	Q2AS

*1 : Characters in parentheses ( ) indicate the special register numbers where individual information is being stored.

Error Code (SD0)*1	Error Messages	Common Information (SD5 to 15)	Individual Information (SD16 to 26)	LED Status		Operating Statuses of CPU	Diagnostic Timing	
				RUN	ERROR			
1310 1311	I/O INT. ERROR	----	----	Off	Flicker	Stop	During interrupt	
1401	SP. UNIT DOWN	Unit No.	----	Off/ On	Flicker/ On	Stop/ Continue*6	At power ON/ At reset/When intelligent function module is accessed.	
	SP. UNIT DOWN	Unit No.	----	Off/ On	Flicker/ On	Stop/ Continue*6	At power ON/ At reset	

*1 : Characters in parentheses ( ) indicate the special register numbers where individual information is being stored.

*6 : Stop/continue operation is selectable for each module by setting parameters.

	Error Code (SD0)*1	Error Contents and Cause	Corrective Action	Corresponding CPU
	1310	An interruption has occurred although there is no interrupt module.	Any of the mounted modules is experiencing a hardware fault. Therefore, check the mounted modules and change the faulty module. (Contact your local Mitsubishi representative.)	○
	1311	An interrupt request from other than the interrupt module was detected.	Take action so that an interrupt will not be issued from other than the interrupt module.	Q00J/Q00/ Q01*13
		An interrupt request from the module where interrupt pointer setting has not been made in the PLC parameter dialog box was detected.	• Correct the interrupt pointer setting in the PLC system setting of the PLC parameter dialog box. • Take measures so that an interrupt is not issued from the module where the interrupt pointer setting in the PLC system setting of the PLC parameter dialog box has not been made.Correct the interrupt setting of the network parameter.Correct the interrupt setting of the intelligent function module buffer memory.Correct the basic program of the QD51.	Q00J/Q00/Q01 *12 QnPRH
	1401	• There was no response from the intelligent function module/special function module during initial communication stage. • The size of the buffer memory of the intelligent function module is wrong.	This suggests a CPU module hardware fault. (Contact your local Mitsubishi representative.)	QCPU Rem
		When PLC parameter I/O allocation was being made, there was no return signal from the special function module during initial processing stage.(When error is generated, the head I/O number of the special function module that corresponds to the common information is stored.)	The special function module that was accessed is experiencing a hardware fault. (Contact your local Mitsubishi representative.)	QnA

*1 : Characters in parentheses ( ) indicate the special register numbers where individual information is being stored.

*12 : This applies to the CPU module of function version B or later.

*13 : This applies to the CPU module of function version A.

Error Code (SD0)*1	Error Messages	Common Information (SD5 to 15)	Individual Information (SD16 to 26)	LED Status		Operating Statuses of CPU	Diagnostic Timing	
				RUN	ERROR			
1402	SP. UNIT DOWN	Unit No.	Program error location	Off/ On	Flicker/ On	Stop/ Continue*6	When an intelligent function module access instruction is executed.	
	SP. UNIT DOWN	Unit No.	Program error location	Off/ On	Flicker/ On	Stop/ Continue*6	During execution of FROM/TO instruction set.	
1403	SP. UNIT DOWN	Unit No.	----	Off/ On	Flicker/ On	Stop/ Continue*6	Always	
	SP. UNIT DOWN	Unit No.	----	Off/ On	Flicker/ On	Stop/ Continue*6	When an END instruction executed.	

*1 : Characters in parentheses ( ) indicate the special register numbers where individual information is being stored.

*6 : Stop/continue operation is selectable for each module by setting parameters.

	Error Code (SD0)*1	Error Contents and Cause	Corrective Action	Corresponding CPU
	1402	The intelligent function module/special function module was accessed in the program, but there was no response.	This suggests a CPU module hardware fault. (Contact your local Mitsubishi representative.)	QCPU Rem
		The special function module was accessed during the execution of a FROM/TO instruction set, but there was no response. (When an error is generated, the program error location corresponding to the individual information is stored.)	The special function module that was accessed is experiencing a hardware fault.(Contact your local Mitsubishi representative.)	QnA
	1403	• There was no response from the intelligent function module when the END instruction is executed. • An error is detected at the intelligent function module. • The I/O module (intelligent function module) is nearly removed, completely removed, or mounted during running.	The intelligent function module that was accessed is experiencing a hardware fault. (Contact your local Mitsubishi representative.)	Q00J/Q00/Q01
		• There was no response from the intelligent function module/special function module when the END instruction is executed. • An error is detected at the intelligent function module/special function module. • The I/O module (intelligent function module/special function module) is nearly removed, completely removed, or mounted during running.	The intelligent function module/special function module that was accessed is experiencing a hardware fault. (Contact your local Mitsubishi representative.)	Qn(H) QnPH QnPRH Rem

*1 : Characters in parentheses ( ) indicate the special register numbers where individual information is being stored.

Error Code (SD0)*1	Error Messages	Common Information (SD5 to 15)	Individual Information (SD16 to 26)	LED Status		Operating Statuses of CPU	Diagnostic Timing	
				RUN	ERROR			
1411	CONTROL-BUS. ERR.	Unit No.	----	Off	Flicker	Stop	At power ON/ At reset	
1412	CONTROL-BUS. ERR.	Unit No.	Program error location	Off	Flicker	Stop	During execution of FROM/TO instruction set.	
1413	CONTROL-BUS. ERR.	----	----	Off	Flicker	Stop	Always	

*1 : Characters in parentheses ( ) indicate the special register numbers where individual information is being stored.

	Error Code (SD0)*1	Error Contents and Cause	Corrective Action	Corresponding CPU
	1411	When performing a parameter I/O allocation the intelligent function module/special function module could not be accessed during initial communications. (On error occurring, the head I/O number of the corresponding intelligent function module/special function module is stored in the common information.)	Reset the CPU module and RUN it again. If the same error is displayed again, the intelligent function module/ special function module, CPU module or base unit is faulty. (Contact your local Mitsubishi representative.)	○ Rem
	1412	The FROM/TO instruction set could not be executed, due to a system bus error with the intelligent function module/ special function module. (On error occurring, the program error location is stored in the individual information.)		○
	1413	In a multiple CPU system, a CPU module incompatible with the multiple CPU system is mounted.	• Remove the CPU module incompatible with the multiple CPU system from the main base unit, or replace the CPU module incompatible with the multiple CPU system with a CPU module compatible with the multiple CPU system. • The intelligent function module, CPU module or base unit is faulty. (Contact your local Mitsubishi representative.)	Q00J/Q00/ Q01*12 Qn(H)*12 QnPH
		An error is detected on the system bus. • Self-diagnosis error of the system bus. • Self-diagnosis error of the CPU module	Reset the CPU module and RUN it again. If the same error is displayed again, the intelligent function module, CPU module or base unit is faulty. (Contact your local Mitsubishi representative.)	QCPU Rem

*1 : Characters in parentheses ( ) indicate the special register numbers where individual information is being stored.

*12 : This applies to the CPU module of function version B or later.

Error Code (SD0)*1	Error Messages	Common Information (SD5 to 15)	Individual Information (SD16 to 26)	LED Status		Operating Statuses of CPU	Diagnostic Timing	
				RUN	ERROR			
1414	CONTROL-BUS. ERR.	Unit No.	----	Off	Flicker	Stop	When an END instruction executed.	
1415	CONTROL-BUS. ERR.	Base No.	----	Off	Flicker	Stop	When an END instruction executed.	
1416	CONTROL-BUS. ERR.	Unit No.	----	Off	Flicker	Stop	At power ON/ At reset	
1417	CONTROL-BUS. ERR.	----	----	Off	Flicker	Stop	Always	
1421	SYS. UNIT DOWN *3	----	----	Off	Flicker	Stop	Always	

*1 : Characters in parentheses ( ) indicate the special register numbers where individual information is being stored.

*3 : This can only be detected in redundant systems. Detection is possible in either the control system or the standby system.

	Error Code (SD0)*1	Error Contents and Cause	Corrective Action	Corresponding CPU
	1414	• Fault of the loaded module was detected. • In a multiple CPU system, a CPU module incompatible with the multiple CPU system is mounted.	• Remove the CPU module incompatible with the multiple CPU system from the main base unit, or replace the CPU module incompatible with the multiple CPU system with a CPU module compatible with the multiple CPU system. • Reset the CPU module and RUN it again. If the same error is displayed again, the intelligent function module, CPU module or base unit is faulty. (Contact your local Mitsubishi representative.)	Q00J/Q00/ Q01*12 Qn(H)*12 QnPH
		An error is detected on the system bus.	Reset the CPU module and RUN it again. If the same error is displayed again, the intelligent function module, CPU module or base unit is faulty. (Contact your local Mitsubishi representative.)	Q00J/Q00/ Q01*12 Qn(H) QnPH QnPRH Rem
	1415	Fault of the main or extension base unit was detected.	Reset the CPU module and RUN it again. If the same error is displayed again, the intelligent function module, CPU module or base unit is faulty. (Contact your local Mitsubishi representative.)	Q00J/Q00/Q01 Qn(H)*12 QnPH QnPRH
	1416	System bus fault was detected at PLC power-on or CPU module reset.		Qn(H)*12 QnPH
		In a multiple CPU system, a bus fault was detected at power-on or reset.	Reset the CPU module and RUN it again. If the same error is displayed again, the intelligent function module, CPU module or base unit is faulty. (Contact your local Mitsubishi representative.)	Q00/Q01*12
	1417	A reset signal error was detected on the system bus.		QnPRH
	1421	Hardware fault at the system management module AS92R.	This suggests a system management module AS92R hardware fault. (Contact your local Mitsubishi representative.)	Q4AR

*1 : Characters in parentheses ( ) indicate the special register numbers where individual information is being stored.

*12 : This applies to the CPU module of function version B or later.

Error Code (SD0)*1	Error Messages	Common Information (SD5 to 15)	Individual Information (SD16 to 26)	LED Status		Operating Statuses of CPU	Diagnostic Timing	
				RUN	ERROR			
1500	AC/DC DOWN	----	----	On	Off	Continue	Always	
1510	DUAL DC DOWN 5V*4	----	----	On	On	Continue	Always	
	SINGLE PS. DOWN	Base No./ Power supply No.	----	On	On	Continue	Always	
1520	DC DOWN 5V*5	----	----	Off	Flicker	Stop	Always	
	SINGLE PS. ERROR	Base No./ Power supply No.	----	On	On	Continue	Always	
1530	DC DOWN 24V*3	----	----	On	On	Continue	Always	
1600	BATTERY ERROR*16	Drive Name	----	On	Off	Continue	Always	
1601								
1602					On			

*1 : Characters in parentheses ( ) indicate the special register numbers where individual information is being stored.

*3 : This can only be detected in redundant systems. Detection is possible in either the control system or the standby system.

*4 : This can only be detected in the redundant system control system.

*5 : This can be detected in either a standalone system or a redundant system. However, in a redundant system it can only be detected in the control system.

*16 : When BATTERY ERROR occurs, the BAT. ALM LED turns on.

	Error Code (SD0)*1	Error Contents and Cause	Corrective Action	Corresponding CPU
	1500	• A momentary power supply interruption has occurred. • The power supply went off.	Check the power supply.	○ Rem
	1510	The power supply voltage (100 to 240VAC) of either of the two power supply modules on the power supply duplexing extension base unit dropped to or below 85% of the rated voltage.	Check the supply voltage of the power supply module. If the voltage is abnormal then replace the power supply module.	Q4AR
		The power supply voltage of either power supply module dropped in the redundant base unit.	Check the power supplied to the redundant power supply modules mounted on the redundant base units.	QnPRH Rem
	1520	The power supply voltage(100 - 240VAC) of the power supply module on the extension base unit dropped to or below 85% of the rated voltage.	Check the supply voltage of the power supply module. If the voltage is abnormal then replace the power supply module.	Q4AR
		A fault of either redundant power supply module was detected in the redundant power supply system.	Hardware fault of the redundant power supply module. (Contact your local Mitsubishi representative, explaining a detailed description of the problem.)	QnPRH Rem
	1530	The 24 VDC power supplied to the system management module AS92R has dropped below 90% of the rated voltage.	Check the 24VDC power supplied to the system management module AS92R.	Q4AR
	1600	• Voltage in the CPU module battery has dropped below stipulated level. • The CPU module battery is not connected.	• Change the battery. • If the battery is for program memory, standard RAM or for the back-up power function, install a lead connector.	○
	1601	Voltage of the battery on memory card 1 has dropped below stipulated level.	Change the battery.	QnA Qn(H) QnPH QnPRH
	1602	Voltage of the battery on memory card 2 has dropped below stipulated level.	Change the battery.	QnA

*1 : Characters in parentheses ( ) indicate the special register numbers where individual information is being stored.

Error Code (SD0)*1	Error Messages	Common Information (SD5 to 15)	Individual Information (SD16 to 26)	LED Status		Operating Statuses of CPU	Diagnostic Timing	
				RUN	ERROR			
2000	UNIT VERIFY ERR.	Unit No. [For Remote I/O network] Network No./ Station No.	----	Off/ On	Flicker/ On	Stop/ Continue*2	When an END instruction executed.	
2001	UNIT VERIFY ERR.	Unit No.	----	Off/ On	Flicker/ On	Stop/ Continue	When an END instruction executed.	
2010	BASE LAY ERROR	Base No.	----	Off	Flicker	Stop	At power ON/ At reset	
2011								

*1 : Characters in parentheses ( ) indicate the special register numbers where individual information is being stored.

*2 : The CPU module operation status when an error occurs can be set at the parameters. (LED display will change accordingly.)

	Error Code (SD0)*1	Error Contents and Cause	Corrective Action	Corresponding CPU
	2000	In a multiple CPU system, a CPU module incompatible with the multiple CPU system is mounted.	Replace the CPU module incompatible with the multiple CPU system with a CPU module compatible with the multiple CPU system.	Qn(H)*12 QnPH
		I/O module information power ON is changed. • I/O module (or intelligent function module) is not installed properly or installed on the base unit.	Read the error common information at the GX Developer, and check and/or change the module that corresponds to the numerical value (module number) there. Alternatively, monitor special registers SD150 to SD157 using GX Developer, and check and replace the module where the bit of its data is "1".	Q00J/Q00/Q01
		I/O module information power ON is changed. • I/O module (or intelligent function module/special function module) not installed properly or installed on the base unit.	• Read the common information of the error using the peripheral device, and check and/or change the module that corresponds to the numerical value (module number) there. • Alternatively, monitor the special registers SD1400 to SD1431 at a peripheral device, and change the fuse at the output module whose bit has a value of "1". • When a GOT is bus-connected to the main base unit or extension base unit, check the connection status of the extension cable and the earth status of the GOT.	QnA Qn(H) QnPH QnPRH Rem
	2001	During operation, a module was mounted on the slot where the empty setting of the CPU module was made.	During operation, do not mount a module on the slot where the empty setting of the CPU module was made.	Q00J/Q00/Q01*12
	2010	• More than applicable number of extension base units have been used. • When a GOT was bus-connected, the CPU module was reset while the power of the GOT was OFF.	• Use the allowable number of extension base units or less. • Power on the PLC and GOT again.	Q00J/Q00/Q01*12 QnPRH
	2011	The QA□B or QA1S□B was used as the base unit.	Do not use the QA□B or QA1S□B as the base unit.	

*1 : Characters in parentheses ( ) indicate the special register numbers where individual information is being stored.

*12 : This applies to the CPU mdoule of function version B or later.

Error Code (SD0)*1	Error Messages	Common Information (SD5 to 15)	Individual Information (SD16 to 26)	LED Status		Operating Statuses of CPU	Diagnostic Timing	
				RUN	ERROR			
2100	SP. UNIT LAY ERR.	Unit No.	----	Off	Flicker	Stop	At power ON/ At reset	

*1 : Characters in parentheses ( ) indicate the special register numbers where individual information is being stored.

	Error Code (SD0)*1	Error Contents and Cause	Corrective Action	Corresponding CPU
	2100	Slot loaded with the QI60 is set to other than the Inteli (intelligent function module) or Interrupt (interrupt module) in the PLC parameter I/O assignment.	Make setting again to match the PLC parameter I/O assignment with the actual loading status.	Qn(H)*12 QnPH QnPRH
		• In the PLC parameter I/O allocation settings, an Inteli (intelligent function module) was allocated to a location reserved for an I/O module or vice versa. • In the PLC parameter I/O allocation settings, a module other than CPU (or nothing) was allocated to a location reserved for a CPU module or vice versa. • In the I/O assignment setting of the PLC parameter dialog box, switch setting was made to the module without switch setting.	• Reset the PLC parameter I/O allocation setting to conform to the actual status of the intelligent function module and the CPU module. • Delete the switch setting in the I/O assignment setting of the PLC parameter dialog box.	Qn(H) QnPH QnPRH Rem
		• In the parameter I/O allocation settings, an Inteli (intelligent function module) was allocated to a location reserved for an I/O module or vice versa. • In the parameter I/O allocation settings, a module other than CPU (or nothing) was allocated to a location reserved for a CPU module or vice versa. • In the I/O assignment setting of the PLC parameter dialog box, the number of points assigned to the intelligent function module is less than the number of points of the mounted module.	Reset the parameter I/O allocation setting to conform to the actual status of the intelligent function module and the CPU module.	Q00J/Q00/Q01
		In PLC parameter I/O allocation settings, a special function module was allocated to a location reserved for an I/O module. Or, the opposite has happened.	Reset the PLC parameter I/O allocation setting to conform with the actual status of the special function modules.	QnA

*1 : Characters in parentheses ( ) indicate the special register numbers where individual information is being stored.

*12 : This applies to the CPU module of function version B or later.

Error Code (SD0)*1	Error Messages	Common Information (SD5 to 15)	Individual Information (SD16 to 26)	LED Status		Operating Statuses of CPU	Diagnostic Timing	
				RUN	ERROR			
2101 2102 2103	SP. UNIT LAY ERR.	Unit No.	----	Off	Flicker	Stop	At power ON/ At reset	

*1 : Characters in parentheses ( ) indicate the special register numbers where individual information is being stored.

	Error Code (SD0)*1	Error Contents and Cause	Corrective Action	Corresponding CPU
	2101	13 or more A-series special function modules (except for the A1SI61) that can initiate an interrupt to the CPU module have been installed.	Reduce the A series special function modules (except the A1SI61) that can make an interrupt start to the CPU module to 12 or less.	Qn(H)
		13 or more special function modules (not counting the A(1S)I61) capable of sending an interrupt to the CPU module have been installed.	Keep the number of special function modules that can initiate an interrupt (with the exception of the A(1S)I61 module) to 12 or fewer.	QnA
	2102	7 or more A1SD51S have been installed.	Keep the number of A1SD51S to 6 or fewer.	Qn(H)
		7 or more serial communication modules (excludes A (1S) J71QC24) have been installed.	Keep the number of serial communication modules (excludes A(1S)J71QU24) installed to 6 or fewer.	QnA
	2103	• 2 or more QI60/A1SI61 modules are loaded in a single CPU system. • 2 or more QI60/A1SI61 modules are set to the same control CPU in a multiple CPU system. • 2 or more A1SI61 modules are loaded in a multiple CPU system.	• Reduce the number of QI60/A1SI61 modules loaded in the single CPU system to one. • Change the number of QI60/A1SI61 modules set to the same control CPU to only one in the multiple CPU system. • Reduce the number of A1SI61 modules to only one in the multiple CPU system.When using an interrupt module with each QCPU in a multiple CPU system, change it for the QI60. (Use one A1SI61 module + max. three QI60 modules or only the QI60 modules.)	Qn(H)*12 QnPH
		2 or more QI60, A1SI61 interrupt modules have been installed.	Install only 1 QI60, A(1S)I61 module.	Qn(H) QnPRH
		The QI60 is loaded.	Remove the QI60.	Rem
		2 or more A1SI61 interrupt modules have been installed.	Install only 1 AI61 module.	QnA
		2 or more QI60 modules are mounted.	Reduce the QI61 modules to one.	Q00J/Q00/ Q01*14
		2 or more QI60 modules where interrupt pointer setting has not been made are mounted.	• Reduce the QI60 modules to one. • Make interrupt pointer setting to the second QI60 module and later.	Q00J/Q00/ Q01*12

*1 : Characters in parentheses ( ) indicate the special register numbers where individual information is being stored.

*12 : This applies to the CPU module of function version B or later.

*14 : This applies to the CPU module with the serial No. of which first 5 digits are "04101" or later.

Error Code (SD0)*1	Error Messages	Common Information (SD5 to 15)	Individual Information (SD16 to 26)	LED Status		Operating Statuses of CPU	Diagnostic Timing	
				RUN	ERROR			
2104 2105	SP. UNIT LAY ERR.	Unit No.	----	Off	Flicker	Stop	At power ON/ At reset	

*1 : Characters in parentheses ( ) indicate the special register numbers where individual information is being stored.

	Error Code (SD0)*1	Error Contents and Cause	Corrective Action	Corresponding CPU
	2104	At the MELSECNET/MINI auto refresh network parameter settings, the module allocation that was set is different from the actual module models at the station numbers in the link system.	Reset the network parameter MELSECNET/MINI auto refresh unit module allocation setting so that it conforms to the station number of the module that is actually linked.	QnA
	2105	There are too many special function modules that can use dedicated instructions allocated (number of modules installed). (The total of the figures indicated below is above 1344.) (AD59 modules installed × 5) (AD57(S1)/AD58 modules installed × 8) (AJ71C24(S3/S6/S8) modules installed × 10) (AJ71UC24 modules installed × 10) (AJ71C21(S1) modules installed × 29) (AJ71PT32-S3/AJ71T32-S3 modules installed ×125) * (AJ71QC24(R2,R4) modules installed × 29) (AJ71ID1(2)-R4 modules installed × 8) +(AD75 modules installed × 12) total ＞1344 *: When the expansion mode is used.	Reduce the number of special function modules installed.	

*1 : Characters in parentheses ( ) indicate the special register numbers where individual information is being stored.

Error Code (SD0)*1	Error Messages	Common Information (SD5 to 15)	Individual Information (SD16 to 26)	LED Status		Operating Statuses of CPU	Diagnostic Timing	
				RUN	ERROR			
2106	SP. UNIT LAY ERR.	Unit No.	----	Off	Flicker	Stop	At power ON/ At reset	

*1 : Characters in parentheses ( ) indicate the special register numbers where individual information is being stored.

	Error Code (SD0)*1	Error Contents and Cause	Corrective Action	Corresponding CPU
	2106	• 5 or more MELSECNET/H modules are loaded in a whole multiple CPU system. • 5 or more Q series Ethernet interface modules are loaded in a whole multiple CPU system.	• Reduce the MELSECNET/H modules to 4 or less in the whole multiple CPU system. • Reduce the Q series Ethernet modules to 4 or less in the whole multiple CPU system.	Qn(H)*12 QnPH
		• 5 or more MELSECNET/H modules have been installed. • 5 or more Q series Ethernet interface modules have been installed. • The same network numbers or station numbers exist, in the MELSECNET/10 network system.	• Reduce the MELSECNET/H modules to 4 or less. • Reduce the Q series Ethernet modules to 4 or less. • Check the network numbers and station numbers.	Qn(H) QnPH QnPRH Rem
		• 2 or more MELSECNET/H modules were installed. • 2 or more Q series Ethernet modules were installed. • 3 or more Q series CC-Link modules were installed. • The same network number or same station number exists in the MELSECNET/H network system.	• Reduce the MELSECNET/H modules to one or less. • Reduce the Q series Ethernet modules to one or less. • Reduce the Q series CC-Link modules to two or less. • Check the network number and station number.	Q00J/Q00/Q01
		• 5 or more AJ71QLP21 & AJ71QBR11 modules are installed. • 3 or more AJ71AP21/R21 & AJ71AT21B modules are installed. • The total number of installed AJ71QLP21, AJ71QBR11, AJ71AP21/R21, and AJ71AT21B modules exceeds 5. • The same network numbers or identical station numbers exist in the MELSECNET/10 network system. • 2 or more master or load stations exist simultaneously at the MELSECNET(II) or MELSECNET/B data link system.	• Reduce the AJ71QLP21 and AJ71QBR11 modules to 4 or less. • Reduce the AJ71AP21/R21 and AJ71AT21B modules to 2 or less. • Reduce the AJ71QLP21, AJ71QBR11, AJ71AP21/R21 and AJ71AT21B modules to a total of 4 or less. • Check the network Nos. and station Nos. • Check the station Nos.	QnA

*1 : Characters in parentheses ( ) indicate the special register numbers where individual information is being stored.

*12 : This applies to the CPU module of function version B or later.

Error Code (SD0)*1	Error Messages	Common Information (SD5 to 15)	Individual Information (SD16 to 26)	LED Status		Operating Statuses of CPU	Diagnostic Timing	
				RUN	ERROR			
2107	SP. UNIT LAY ERR.	Unit No.	----	Off	Flicker	Stop	At power ON/ At reset	
2108								
2109						Stop/ Continue*6		
2110	SP. UNIT ERROR	Unit No.	Program error location	Off/ On	Flicker/ On	Stop/ Continue*2	When instruction executed.	
2111								

*1 : Characters in parentheses ( ) indicate the special register numbers where individual information is being stored.

*2 : The CPU module operation status when an error occurs can be set at the parameters. (LED display will change accordingly.)

*6 : Stop/continue operation is selectable for each module by setting parameters.

	Error Code (SD0)*1	Error Contents and Cause	Corrective Action	Corresponding CPU
	2107	Head X/Y set in the PLC parameter I/O allocation settings is also the head X/Y for another module.	Reset the PLC parameter I/O allocation setting to conform with the actual status of the special function modules.	○ Rem
	2108	• Network module A1SJ71LP21, A1SJ71BR11, A1SJ71AP21, A1SJ71AR21, or A1SJ71AT21B dedicated for the A2USCPU has been installed. • Network module A1SJ71QLP21 or A1SJ71QBR11 dedicated for the Q2AS has been installed.	Change network module to MELSECNET/H module.	Qn(H)
		A(1S)J71LP21 or A(1S)J71BR11 for use with the AnUCPU network module has been installed.	Change network module to A(1S)J71QLP21 or A(1S)J71QBR11.	QnA
	2109	The control system and standby system module configurations are different when a redundant system is in the backup mode.	Check the module configuration of the standby system.	Q4AR
	2110	• The location designated by the FROM/TO instruction set is not the intelligent function module/special function module. • The module that does not include buffer memory has been specified by the FROM/TO instruction. • The intelligent function module/ special function module, Network module being accessed is faulty. • Station not loaded was specified using the instruction whose target was the CPU share memory.	• Read the individual information of the error using the GX Developer, check the FROM/TO instruction that corresponds to that numerical value (program error location), and correct when necessary. • The intelligent function module/ special function module that was accessed is experiencing a hardware fault. Therefore, change the faulty module. Alternatively, contact your local Mitsubishi representative.	Q00J/Q00/Q01 Qn(H)*12 QnPH QnPRH
	2111	• The location designated by a link direct device (J□\□) is not a network module. • The I/O module (intelligent function module/special function module) was nearly removed, completely removed, or mounted during running.		○

*1 : Characters in parentheses ( ) indicate the special register numbers where individual information is being stored.

*12 : This applies to the CPU module of function version B or later.

Error Code (SD0)*1	Error Messages	Common Information (SD5 to 15)	Individual Information (SD16 to 26)	LED Status		Operating Statuses of CPU	Diagnostic Timing	
				RUN	ERROR			
2112	SP. UNIT ERROR	Unit No.	Program error location	Off/ On	Flicker/ On	Stop/ Continue*2	When instruction executed/ STOP → RUN	
2113	SP. UNIT ERROR	$FFFF_H$ (fixed)	Program error location	Off/ On	Flicker/ On	Stop/ Continue*2	When instruction executed/ STOP → RUN	

*1 : Characters in parentheses ( ) indicate the special register numbers where individual information is being stored.

*2 : The CPU module operation status when an error occurs can be set at the parameters. (LED display will change accordingly.)

	Error Code (SD0)*1	Error Contents and Cause	Corrective Action	Corresponding CPU
		• The module other than special function module is specified by the special function module dedicated instruction. Or, it is not the corresponding special function module. • The module model specified by the special function module dedicated instruction and that specified by the parameter I/O assignment is different.	• Read the individual information of the error using a peripheral device, and check the special function module dedicated instruction (network instruction) that corresponds to the value (program error part) to make modification. • Set the module model by PLC parameter I/O assignment according to the special function module dedicated instruction setting. Example) Although AJ71QC24N is used actually, AJ71QC24 is set.	QnA
	2112	• The module other than intelligent function module/special function module is specified by the intelligent function module/special function module dedicated instruction. Or, it is not the corresponding intelligent function module/special function module. • There is no network No. specified by the network dedicated instruction. Or the relay target network does not exit. • The model is incorrectly registered by the dedicated instruction.	Read the individual information of the error using a peripheral device, and check the special function module / special function module dedicated instruction (network instruction) that corresponds to the value (program error part) to make modification.	QCPU Rem
	2113	The module other than network module is specified bythe network dedicated instruction.		QnA Qn(H) QnPH

*1 : Characters in parentheses ( ) indicate the special register numbers where individual information is being stored.

Error Code (SD0)*1	Error Messages	Common Information (SD5 to 15)	Individual Information (SD16 to 26)	LED Status		Operating Statuses of CPU	Diagnostic Timing	
				RUN	ERROR			
2114	SP. UNIT ERROR	Unit No.	Program error location	Off/ On	Flicker/ On	Stop/ Continue	When instruction executed.	
2115								
2116								
2117								
2118								

*1 : Characters in parentheses ( ) indicate the special register numbers where individual information is being stored.

	Error Code (SD0)*1	Error Contents and Cause	Corrective Action	Corresponding CPU
	2114	An instruction, which on execution specifies other stations, has been used for specifying the host CPU. (An instruction that does not allow the host CPU to be specified).	Read the individual information of the error using the GX Developer, check the program corresponding that value (program error location), and make correction.	Q00J/Q00/ Q01*12 Qn(H)*12 QnPH
	2115	An instruction, which on execution specifies the host CPU, has been used for specifying other CPUs. (An instruction that does not allow other stations to be specified).		
	2116	• An instruction that does not allow the .under the control of another CPU to be specified is being used for a similar task. • Instruction was executed for the A or QnA module under control of another CPU.		
	2117	A CPU module that cannot be specified in the instruction dedicated to the multiple CPU system was specified.		
	2118	When the online module change setting has been set to enable in the PLC parameter of the Process CPU in a multiple CPU system, the intelligent function module controlled by another CPU has been specified in the FROM instruction/intelligent function module device (U□\G□).	• When online module change of the Process CPU is to be performed in a multiple CPU system, correct the program so that access will not be made to the intelligent function module controlled by another CPU. • When access is to be made to the intelligent function module controlled by another CPU in a multiple CPU system, set the online module change setting to disable in the parameter of the Process CPU.	Qn(H)*12 QnPH

*1 : Characters in parentheses ( ) indicate the special register numbers where individual information is being stored.

*12 : This applies to the CPU module of function version B or later.

Error Code (SD0)*1	Error Messages	Common Information (SD5 to 15)	Individual Information (SD16 to 26)	LED Status		Operating Statuses of CPU	Diagnostic Timing	
				RUN	ERROR			
2120 2121 2122 2123 2124	SP. UNIT LAY ERR.	----	----	Off	Flicker	Stop	At power ON/ At reset	

*1 : Characters in parentheses ( ) indicate the special register numbers where individual information is being stored.

	Error Code (SD0)*1	Error Contents and Cause	Corrective Action	Corresponding CPU
	2120	The location of Q□B and QA1S□B is improper.	Check the location of the base unit.	Q00J/Q00/ Q01*13 Qn(H) QnPH
	2121	The CPU module is installed at other than the CPU slot or slots 0 to 2.	Check the loading position of the CPU module and reinstall it at the correct slot.	Qn(H) QnPH Rem
	2122	QA1S□B is used to the main base unit.	Replace the main base unit with a usable one.	Qn(H) QnPH QnPRH Rem
	2124	• A module is installed at 65th or higher slot. • A module is installed at the slot later than the number of slots specified with base allocation setting. • A module is installed at the I/O points later than the 4,096th point. • A module installed at the 4,096th point occupies higher points.	• Remove the module installed at 65th or later slot. • Remove the module installed at the slot later than the number of slots specified with base allocation setting. • Remove the module installed at the I/O points later than the 4,096th point. • Change the last module to a module which does not exceed the 4,096th point.	Q00J/Q00/Q01 Rem
		• A module is mounted on the 25th or higher slot. (17th or higher slots for the Q00J) • A module is mounted on the slot higher than the number specified with base allocation setting. • A module is mounted on the I/O points higher than the 1024th point. (256th or higher points for the Q00J) • A module is mounted on the 1024th or higher points. (256th or higher points for the Q00J)	• Remove the module mounted on the 25th or higher slot. (17th or higher slots for the Q00J) • Remove the module mounted on the slot higher than the number specified with base allocation setting. • Remove the module mounted on the I/O points higher than the 1024th point. (256th or higher points for the Q00J) • Change the last module with the one which does not exceed the 1024th point. (256th or lower points for the Q00J)	Q00J/Q00/Q01 Rem
		5 or more extension base units were added. (3 bases for Q00J)	Remove 5 or more extension base units. (3 bases for Q00J)	Q00J/Q00/ Q01*13 Rem

*1 : Characters in parentheses ( ) indicate the special register numbers where individual information is being stored.

*13 : This applies to the CPU module of function version A.

Error Code (SD0)*1	Error Messages	Common Information (SD5 to 15)	Individual Information (SD16 to 26)	LED Status		Operating Statuses of CPU	Diagnostic Timing	
				RUN	ERROR			
2125 2126	SP. UNIT LAY. ERR.	Unit No.	----	Off	Flicker	Stop	At power ON/ At reset	
2150 2151	SP. UNIT VER. ERR.	Unit No.	----	Off	Flicker	Stop	At power ON/ At reset	
2200	MISSING PARA.	Drive Name	----	Off	Flicker	Stop	At power ON/ At reset	

*1 : Characters in parentheses ( ) indicate the special register numbers where individual information is being stored.

	Error Code (SD0)*1	Error Contents and Cause	Corrective Action	Corresponding CPU
	2125	• A module which the QCPU cannot recognise has been installed. • There was no response form the intelligent function module/special function module.	• nstall a usable module. • The intelligent function module/ special function module is experiencing a hardware fault. (Contact your local Mitsubishi representative.)	QCPU Rem
	2126	CPU module locations in a multiple CPU system are either of the following. • There are empty slots between the QCPU and QCPU/motion controller. • A module other than the High Performance model QCPU/Process CPU (including the motion controller) is mounted on the left-hand side of the High Performance model QCPU/ Process CPU.	• Mount modules on the available slots so that the empty slots will be located on the right-hand side of the CPU module.) • Remove the module mounted on the left-hand side of the High Performance model QCPU/Process CPU, and mount the High Performance model QCPU/Process CPU on the empty slot. Mount the motion CPU on the right-hand side of the High Performance model QCPU/Process CPU.	Qn(H)*12 QnPH
	2150	In a multiple CPU system, the control CPU of the intelligent function module incompatible with the multiple CPU system is set to other than CPU No.1.	• Change the intelligent function module for the one compatible with the multiple CPU system (function version B). • Change the setting of the control CPU of the intelligent function module incompatible with the multiple CPU system to CPU No.1.	
	2151	Either of the following modules incompatible with the redundant system has been mounted in a redundant system. • MELSECNET/H • Ethernet	Use either of the following modules compatible with the redundant system. • MELSECNET/H • Ethernet	QnPRH
	2200	There is no parameter file in the drive specified as valid parameter drive by the DIP switches.	• Check and correct the valid parameter drive settings made by the DIP switches. • Set the parameter file to the drive specified as valid parameter drive by the DIP switches.	Qn(H) QnPH QnPRH
		There is no parameter file at the program memory.	Set the parameter file to the program memory.	Q00J/Q00/Q01

*1 : Characters in parentheses ( ) indicate the special register numbers where individual information is being stored.

*12 : This applies to the CPU module of function version B or later.

Error Code (SD0)*1	Error Messages	Common Information (SD5 to 15)	Individual Information (SD16 to 26)	LED Status		Operating Statuses of CPU	Diagnostic Timing	
				RUN	ERROR			
2210	BOOT ERROR	Drive name	----	Off	Flicker	Stop	At power ON/ At reset	
2211								
2300	ICM. OPE. ERROR	Drive name	----	Off/ On	Flicker/ On	Stop/ Continue*2	When memory card is inserted or removed	
2301								
2302								

*1 : Characters in parentheses ( ) indicate the special register numbers where individual information is being stored.

*2 : The CPU module operation status when an error occurs can be set at the parameters. (LED display will change accordingly.)

	Error Code (SD0)*1	Error Contents and Cause	Corrective Action	Corresponding CPU
	2210	The contents of the boot file are incorrect.	Check the boot setting.	Q00J/Q00/ Q01*12 Qn(H) QnPH QnPRH
		There is no boot file in the drive designated by the parameter enabled drive switch even though the Boot DIP switch is ON.	Check and correct the valid parameter drive settings made by the DIP switches. Set the boot file to the drive specified by the parameter drive DIP switches.	QnA
	2211	File formatting failed at a boot.	• Reboot. • CPU module hardware fault. (Contact your local Mitsubishi representative, explaining a detailed description of the problem.)	QnPRH
	2300	• A memory card was removed without switching the memory card in/out switch OFF. • The memory card in/out switch is turned ON although a memory card is not actually installed.	• Remove memory card after placing the memory card in/out switch OFF. • Turn on the card insert switch after inserting a memory card.	QnA Qn(H) QnPH QnPRH
	2301	• The memory card has not been formatted. • Memory card format status is incorrect.	• Format memory card. • Reformat memory card. • Check memory card.	
	2302	A memory card that cannot be used with the CPU module has been installed.		

*1 : Characters in parentheses ( ) indicate the special register numbers where individual information is being stored.

Error Code (SD0)*1	Error Messages	Common Information (SD5 to 15)	Individual Information (SD16 to 26)	LED Status		Operating Statuses of CPU	Diagnostic Timing	
				RUN	ERROR			
2400	FILE SET ERROR	File name/ Drive name	Parameter number	Off	Flicker	Stop	At power ON/ At reset	
2401	FILE SET ERROR	File name/ Drive name	Parameter number	Off	Flicker	Stop	At power ON/ At reset	

*1 : Characters in parentheses ( ) indicate the special register numbers where individual information is being stored.

	Error Code (SD0)*1	Error Contents and Cause	Corrective Action	Corresponding CPU
	2400	Automatic write to standard ROM was performed on the CPU module that is incompatible with automatic write to standard ROM. (Memory card where automatic write to standard ROM was selected in the boot file was fitted and the parameter enable drive was set to the memory card.)	• Execute automatic write to standard ROM on the CPU module which is compatible with automatic write to standard ROM. • Using GX Developer, perform write of parameters and programs to standard ROM. • Change the memory card for the one where automatic write to standard ROM has not been set, and perform boot operation from the memory card.	Qn(H)*12 QnPH QnPRH
		The file designated at the PLC file settings in the parameters cannot be found.	• Read the individual information of the error using peripheral device, check to be sure that the parameter drive name and file name correspond to the numerical values there (parameter number), and correct. • Create a file created using parameters, and load it to the CPU module.	○
		The Ethernet parameter that was added for QnACPU, with the function version "B," has been set to QnACPU without the function version "B."	Change to QnACPU with the function version "B". Delete the Ethernet parameter.	QnA
	2401	Program memory capacity was exceeded by performing boot operation or automatic write to standard ROM.	• Check and correct the parameters (boot setting). • Delete unnecessary files in the program memory. • Choose "Clear program memory" for boot in the parameter so that boot is started after the program memory is cleared.	Qn(H)*12 QnPH QnPRH
		The file designated at the parameter PLC RAS settings fault history area has not been created.	• Read the individual information of the error using the peripheral device, check to be sure that the parameter drive name and file name correspond to the numerical values there (parameter number), and correct. • Check the space remaining in the memory card.	○

*1 : Characters in parentheses ( ) indicate the special register numbers where individual information is being stored.

*12 : This applies to the CPU module of function version B or later.

Error Code (SD0)*1	Error Messages	Common Information (SD5 to 15)	Individual Information (SD16 to 26)	LED Status		Operating Statuses of CPU	Diagnostic Timing	
				RUN	ERROR			
2402	FILE SET ERROR	File name/ Drive Name	Parameter number	Off	Flicker	Stop	At power ON/ At reset	
2410	FILE OPE. ERROR	File name/ Drive name	Program error location	Off/ On	Flicker/ On	Stop/ Continue*2	When instruction executed.	
2411								
2412								
2413								

*1 : Characters in parentheses ( ) indicate the special register numbers where individual information is being stored.

*2 : The CPU module operation status when an error occurs can be set at the parameters. (LED display will change accordingly.)

	Error Code (SD0)*1	Error Contents and Cause	Corrective Action	Corresponding CPU
	2402	Though the file register has been set in the pairing setting/tracking setting, the file register does not exist.	Confirm the file register and parameter.	Q4AR
	2410	The specified program does not exist in the program memory. This error may occur when the ECALL, EFCALL, PSTOP, PSCAN, POFF or PLOW instruction is executed.	Read the individual information of the error using the peripheral device, check to be sure that the program corresponds to the numerical values there (program location), and correct. Create a file created using parameters, and load it to the CPU module.	QnA Qn(H) QnPH QnPRH
	2411	The specified program exists in the program memory, but has not been registered in the program setting of the Parameter dialog box. This error may occur when the ECALL, EFCALL, PSTOP, PSCAN, POFF or PLOW instruction is executed.	Read the individual information of the error using the peripheral device, check to be sure that the program corresponds to the numerical values there (program location), and correct.	
	2412	The SFC program file is one that cannot be designated by the sequence program.	Read the individual information of the error using the peripheral device, check to be sure that the program corresponds to the numerical values there (program location), and correct.	
	2413	No data has been written to the file designated by the sequence program.	Read the individual information of the error using the peripheral device, check to be sure that the program corresponds to the numerical values there (program location), and correct. Check to ensure that the designated file has not been write protected.	

*1 : Characters in parentheses ( ) indicate the special register numbers where individual information is being stored.

Error Code (SD0)*1	Error Messages	Common Information (SD5 to 15)	Individual Information (SD16 to 26)	LED Status		Operating Statuses of CPU	Diagnostic Timing	
				RUN	ERROR			
2500	CAN'T EXE. PRG.	File name/ Drive name	----	Off	Flicker	Stop	At power ON/ At reset	
2501								
2502								
2503								
2504								

*1 : Characters in parentheses ( ) indicate the special register numbers where individual information is being stored.

	Error Code (SD0)*1	Error Contents and Cause	Corrective Action	Corresponding CPU
	2500	• There is a program file that uses a device that is out of the range set in the PLC parameter device setting. • After the PLC parameter setting is changed, only the parameter is written into the PLC.	• Read the common information of the error using the peripheral device, check to be sure that the parameter device allocation setting and the program file device allocation correspond to the numerical values there (file name), and correct if necessary. • If PLC parameter device setting is changed, batch-write the parameter and program file into the PLC.	○
	2501	There are multiple program files although "none" has been set at the PLC parameter program settings.	Edit the PLC parameter program setting to "yes". Alternatively, delete unneeded programs.	QnA Qn(H) QnPH QnPRH
		• There are three or more program files. • The program name differs from the program contents.	• Delete unnecessary program files. • Match the program name with the program contents.	Q00J/Q00/Q01
	2502	The program file is incorrect. Alternatively, the file contents are not those of a sequence program.	Check whether the program version is＊＊＊.QPG, and check the file contents to be sure they are for a sequence program.	○
		The program file is not the one for the redundant CPU.	Create a program using GX Developer or PX Developer for which the PLC type has been set to the redundant CPU (Q12PRH/Q25PRH), and write it to the CPU module.	QnPRH
	2503	There are no program files at all.	• Check program configuration. • Check parameters and program configuration.	○
	2504	Two or more SFC normal programs or control programs have been designated.		QnA Qn(H) QnPH QnPRH
		There are two or more SFC programs.	Reduce the SFC programs to one.	Q00J/Q00/Q01*12

*1 : Characters in parentheses ( ) indicate the special register numbers where individual information is being stored.

*12 : This applies to the CPU module of function version B or later.

Error Code (SD0)*1	Error Messages	Common Information (SD5 to 15)	Individual Information (SD16 to 26)	LED Status		Operating Statuses of CPU	Diagnostic Timing	
				RUN	ERROR			
3000 3001 3002	PARAMETER ERROR	File name/ Drive name	Parameter number	Off	Flicker	Stop	At power ON/ At reset/ STOP→RUN	
3003	PARAMETER ERROR	File name/ Drive name	Parameter number	Off	Flicker	Stop	When an END instruction executed.	
	PARAMETER ERROR	File name/ Drive name	Parameter number	Off	Flicker	Stop	At power ON/ At reset/ STOP→RUN	

*1 : Characters in parentheses ( ) indicate the special register numbers where individual information is being stored.

	Error Code (SD0)*1	Error Contents and Cause	Corrective Action	Corresponding CPU
		In a multiple CPU system, the intelligent function module under control of another CPU is specified in the interrupt pointer setting of the PLC parameter.	• Specify the head I/O number of the intelligent function module under control of the host CPU. • Delete the interrupt pointer setting of the parameter.	Qn(H)*12 QnPH
	3000	The PLC parameter settings for timer time limit setting, the RUN-PAUSE contact, the common pointer number, number of vacant slots, or system interrupt settings are outside the range that can be used by the CPU module.	• Read the detailed information of the error using the peripheral device, check the parameter items corresponding to those numerical values (parameter numbers), and correct when necessary. • If the error is still generated following the correction of the parameter settings, the possible cause is the memory error of the CPU module's built-in RAM or program memory or the memory card. (Contact your local Mitsubishi representative.)	○ Rem
		In a program memory check, the check capacity has not been set within the range applicable for the CPU module.		
		The parameter settings in the error individual information (special register SD16) are illegal.		
	3001	The parameter settings are corrupted.		
	3002	When "Use the following file" is selected for the file register in the PLC file setting of the PLC parameter dialog box, the specified file does not exist although the file register capacity has been set.		QnA Qn(H) QnPH QnPRH
		The automatic refresh range of the multiple CPU system exceeded the file register capacity.	Change the file register file for the one refresh-enabled in the whole range.	Qn(H)*12 QnPH
	3003	The number of devices set at the PLC parameter device settings exceeds the possible CPU module range.	• Read the detailed information of the error using the peripheral device, check the parameter items corresponding to those numerical values (parameter numbers), and correct when necessary. • If the error is still generated following the correction of the parameter settings, the possible cause is the memory errorm of the CPU module's built-in RAM or program memory or the memory card. (Contact your local Mitsubishi representative.)	○

*1 : Characters in parentheses ( ) indicate the special register numbers where individual information is being stored.

*12 : This applies to the CPU module of function version B or later.

Error Code (SD0)*1	Error Messages	Common Information (SD5 to 15)	Individual Information (SD16 to 26)	LED Status		Operating Statuses of CPU	Diagnostic Timing	
				RUN	ERROR			
3004	PARAMETER ERROR	File name/ Drive name	Parameter number	Off	Flicker	Stop	At power ON/ At reset/ STOP→RUN	
3006	PARAMETER ERROR	File name/ Drive name	Parameter number	Off	Flicker	Stop	At power ON/ At reset/ STOP→RUN	
3007								
3009								

*1 : Characters in parentheses ( ) indicate the special register numbers where individual information is being stored.

	Error Code (SD0)*1	Error Contents and Cause	Corrective Action	Corresponding CPU
	3004	The parameter file is incorrect. Alternatively, the contents of the file are not parameters.	Check whether the parameter file version is ＊＊＊.QPA, and check the file contents to be sure they are parameters.	○
	3006	• The high speed interrupt is set in a Q02CPU. • The high speed interrupt is set in a multiple CPU system. • The high speed interrupt is set when a QA1S□B or QA□B is used. • No module is installed at the I/O address designated by the high speed interrupt.	• Delete the setting of the Q02CPU' s high speed interrupt. To use high speed interrupts, change the CPU module to one of the Q02H/Q06H/ Q12H/Q25HCPU. • To use a multiple CPU system, delete the setting of the high-speed interrupt. To use high speed interrupts, change the system to a single CPU system. • To use either the QA1S□B or QA□B, delete the setting of the high speed interrupt. To use high speed interrupts, do not use the QA1S□B/ QA□B. • Re-examine the I/O address designated by the high speed interrupt setting.	Qn(H)*15
	3007	The parameter file in the drive specified as valid parameter drive by the DIP switches is inapplicable for the CPU module.	Create parameters using GX Developer, and write them to the drive specified as valid parameter drive by the DIP switches.	QnPRH
	3009	In a multiple CPU system, the modules for AnS, A, Q2AS and QnA have been set to multiple control CPUs.	Re-set the parameter I/O assignment to control them under one CPU module. (Change the parameters of all CPUs in the multiple CPU system.)	Qn(H)*12

*1 : Characters in parentheses ( ) indicate the special register numbers where individual information is being stored.

*12 : This applies to the CPU module of function version B or later.

*15 : This applies to the CPU module with the serial No. of which first 5 digits "04102" or later.

Error Code (SD0)*1	Error Messages	Common Information (SD5 to 15)	Individual Information (SD16 to 26)	LED Status		Operating Statuses of CPU	Diagnostic Timing	
				RUN	ERROR			
3010	PARAMETER ERROR	File name/ Drive name	Parameter number	Off	Flicker	Stop	At power ON/ At reset/ STOP→RUN	
3012								
3013								
3014	PARAMETER ERROR	File name/ Drive name	Parameter number	Off	Flicker	Stop	At power ON/ At reset/ STOP→RUN	

*1 : Characters in parentheses ( ) indicate the special register numbers where individual information is being stored.

	Error Code (SD0)*1	Error Contents and Cause	Corrective Action	Corresponding CPU
	3010	The parameter-set number of CPU modules differs from the actual number in a multiple CPU system.	Match the number of (CPU modules in multiple CPU setting) - (CPUs set as empty in I/O assignment) with that of actually mounted CPU modules.	Qn(H)*12 QnPH
	3012	Multiple CPU setting or control CPU setting differs from that of the reference CPU in a multiple CPU system.	Match the multiple CPU setting or control CPU setting in the PLC parameter with that of the reference CPU (CPU No.1).	Q00/Q01*12 Qn(H)*12
	3013	Multiple CPU automatic refresh setting is any of the followings in a multiple CPU system. • When a bit device is specified as a refresh device, a number other than a multiple of 16 is specified for the refresh-starting device. • The device specified is other than the one that may be specified. • The number of send points is an odd number.	Check the following in the multiple CPU automatic refresh parameters and make correction. • When specifying the bit device, specify a multiple of 16 for the refresh starting device. • Specify the device that may be specified for the refresh device. • Set the number of send points to an even number.	Qn(H)*12 QnPH
		In a multiple CPU system, the multiple CPU automatic refresh setting is any of the following. • The total number of transmission points is greater than the maximum number of refresh points.	Check if the following settings have been made in the refresh setting of the multiple CPU setting, and correct the settings as necessary. • The total number of transmission points is within the maximum number of refresh points.	Q00/Q01*12
	3014	• In a multiple CPU system, the online module change parameter (multiple CPU system parameter) settings differ from those of the reference CPU. • In a multiple CPU system, the online module change setting is enabled although the CPU module mounted does not support online module change.	• Match the online module change parameter with that of the reference CPU. • If the CPU module that does not support online module change is mounted, replace it with the CPU module that supports online module change.	QnPH

*1 : Characters in parentheses ( ) indicate the special register numbers where individual information is being stored.

*12 : This applies to the CPU module of function version B or later.

Error Code (SD0)*1	Error Messages	Common Information (SD5 to 15)	Individual Information (SD16 to 26)	LED Status		Operating Statuses of CPU	Diagnostic Timing	
				RUN	ERROR			
3100	LINK PARA. ERROR	File name/ Drive name	Parameter number	Off	Flicker	Stop	At power ON/ At reset/ STOP→RUN	

*1 : Characters in parentheses ( ) indicate the special register numbers where individual information is being stored.

	Error Code (SD0)*1	Error Contents and Cause	Corrective Action	Corresponding CPU
	3100	In a multiple CPU system, the MELSECNET/H under control of another CPU is specified as the head I/O number in the network setting parameter of the MELSECNET/H.	• Delete the MELSECNET/H network parameter of the MELSECNET/H under control of another CPU. • Change the setting to the head I/O number of the MELSECNET/H under control of the host CPU.	Q00/Q01*12 Qn(H)*12 QnPH
		The network parameters of the MELSECNET/H operating in the ordinary station were rewritten to the control station, or the network parameters of the MELSECNET/H operating in the control station were rewritten to the ordinary station.(The network parameters are reflected on the module side by making a reset.)	Reset the CPU module.	Qn(H)*12 QnPH QnPRH
		• The number of actually installed modules is different from that designated in the number of modules setting parameter of MELSECNET/H. • The head I/O number of actually installed modules is different from that designated in the network parameter of MELSECNET/H. • Some data in the parameter cannot be handled. • The station type of MELSECNET/H has been changed while the power is on.(RESET→RUN is required to change the station type.)	• Check the network parameters and mounting status, and if they differ, match the network parameters and mounting status. If any network parameter has been corrected, write it to the CPU module. • Confirm the setting of the number of extension stages of the extension base units. • Check the connection status of the extension base units and extension cables. When the GOT is bus-connected to the main base unit and extension base units, also check their connection status. If the error occurs after the above checks, the possible cause is a hardware fault. (Contact your local Mitsubishi representative, explaining a detailed description of the problem.)	QCPU
		Although the QnACPU is a control station or master station, the network parameters have not been written.	• Correct and write the network parameters. • If the error occurs after correction, it suggests a hardware fault. (Contact your local Mitsubishi representative.)	QnA

*1 : Characters in parentheses ( ) indicate the special register numbers where individual information is being stored.

*12 : This applies to the CPU module of function version B or later.

Error Code (SD0)*1	Error Messages	Common Information (SD5 to 15)	Individual Information (SD16 to 26)	LED Status		Operating Statuses of CPU	Diagnostic Timing	
				RUN	ERROR			
	LINK PARA. ERROR	File name/ Drive name	Parameter number	Off	Flicker	Stop	When an END instruction executed.	
3101	LINK PARA. ERROR	File name/ Drive name	Parameter number	Off	Flicker	Stop	At power ON/ At reset/ STOP→RUN	

*1 : Characters in parentheses ( ) indicate the special register numbers where individual information is being stored.

	Error Code (SD0)*1	Error Contents and Cause	Corrective Action	Corresponding CPU
	3101	The link refresh range exceeded the file register capacity.	Change the file register file for the one that enables entire range refresh.	Qn(H)*12 QnPH QnPRH
		• When the station number of the MELSECNET/H module is 0, the inter-PLC network parameter setting has been made. • When the station number of the MELSECNET/H module is other than 0, the remote master parameter setting has been made.	Correct the type or station number of the MELSECNET/H module in the network parameter to meet the used system.	
		• The network No. specified by a network parameter is different from that of the actually mounted network. • The head I/O No. specified by a network parameter is different from that of the actually mounted I/O unit. • The network class specified by a network parameter is different from that of the actually mounted network. • The network refresh parameter of the MELSECNET/H, MELSECNET/10 is out of the specified area.	• Check the network parameters and mounting status, and if they differ, match the network parameters and mounting status. If any network parameter has been corrected, write it to the CPU module. • Confirm the setting of the number of extension stages of the extension base units. • Check the connection status of the extension base units and extension cables. When the GOT is bus-connected to the main base unit and extension base units, also check their connection status. If the error occurs after the above checks, the cause is a hardware fault. (Contact your local Mitsubishi representative, explaining a detailed description of the problem.)	○
		A multi-remote I/O network was configured using a module that does not support the MELSECNET/H multi-remote I/O network.	Use a module that supports the MELSECNET/H multi-remote I/O network.	QnPH
		• The system A of the MELSECNET/H remote master station has been set to other than Station No. 0. • The system B of the MELSECNET/H remote master station has been set to Station No. 0.	• Set the system A of the MELSECNET/H remote master station to Station No. 0. • Set the system B of the MELSECNET/H remote master station to any of Station No. 1 to 64.	QnPRH

*1 : Characters in parentheses ( ) indicate the special register numbers where individual information is being stored.

*12 : This applies to the CPU module of function version B or later.

Error Code (SD0)*1	Error Messages	Common Information (SD5 to 15)	Individual Information (SD16 to 26)	LED Status		Operating Statuses of CPU	Diagnostic Timing	
				RUN	ERROR			
3102	LINK PARA. ERROR	File name/ Drive name	Parameter number	Off	Flicker	Stop	At power ON/ At reset/ STOP→RUN	
3103	LINK PARA. ERROR	File name/ Drive name	Parameter number	Off	Flicker	Stop	At power ON/ At reset/ STOP→RUN	

*1 : Characters in parentheses ( ) indicate the special register numbers where individual information is being stored.

	Error Code (SD0)*1	Error Contents and Cause	Corrective Action	Corresponding CPU
	3102	• The network module detected a network parameter error. • A MELSECNET/H network parameter error was detected.	• Correct and write the network parameters. • If the error occurs after correction, it suggests a hardware fault. (Contact your local Mitsubishi representative.)	○
		The station No. specified in pairing setting are not correct. • The stations are not numbered consecutively. • Pairing setting has not been made for the CPU module at the normal station.	Refer to the troubleshooting of the network module, and if the error is due to incorrect pairing setting, reexamine the pairing setting of the network parameter.	QnPRH
	3103	In a multiple CPU system, the Q series Ethernet interface module under control of another station is specified as the head I/O number of the Ethernet network parameter.	• Delete the Ethernet network parameter of the Q series Ethernet interface module under control of another station. • Change the setting to the head I/O number of the Q series Ethernet interface module under control of the host station.	Q00/Q01*12 Qn(H)*12 QnPH
		• Although the number of modules has been set to 1 or greater number in the Ethernet module count setting parameter setting, the number of actually mounted module is 0. • The head I/O No. of the Ethernet network parameter differs from the I/O No. of the actually mounted module.	• Correct and write the network parameters. • If the error occurs after correction, it suggests a hardware fault. (Contact your local Mitsubishi representative.)	○ Rem
		• AJ71QE71 does not exist in the position of I/O number set by the parameter. • I/O number designation is overlapping. • Numbers of the network parameter and loaded AJ71QE71 are different. • Ethernet (parameter + dedicated instruction) is set to more than 5.		QnA

*1 : Characters in parentheses ( ) indicate the special register numbers where individual information is being stored.

*12 : This applies to the CPU module of function version B or later.

Error Code (SD0)*1	Error Messages	Common Information (SD5 to 15)	Individual Information (SD16 to 26)	LED Status		Operating Statuses of CPU	Diagnostic Timing	
				RUN	ERROR			
3104	LINK PARA. ERROR	File name/ Drive name	Parameter number	Off	Flicker	Stop	At power ON/ At reset/ STOP→RUN	
3105								
3106	LINK PARA. ERROR	File name/ Drive name	Parameter number	Off	Flicker	Stop	When an END instruction executed.	
	LINK PARA. ERROR	File name	Parameter number	Off	Flicker	Stop	At power ON/ At reset/ STOP→RUN	
3107	LINK PARA. ERROR	File name	Parameter number	Off	Flicker	Stop	At power ON/ At reset/ STOP→RUN	

*1 : Characters in parentheses ( ) indicate the special register numbers where individual information is being stored.

	Error Code (SD0)*1	Error Contents and Cause	Corrective Action	Corresponding CPU
	3104	• Ethernet and MELSECNET/10 use the same network number. • Network number, station number or group number set by the network parameter is out of range. • The I/O No. is specified for the used CPU module. • The Ethernet-specific parameter setting is not normal.	• Correct and write the network parameters. • If the error occurs after correction, it suggests a hardware fault. (Contact your local Mitsubishi representative.)	○ Rem
	3105	In a multiple CPU system, the Q series CC-Link module under control of another station is specified as the head I/O number of the CC-Link network parameter.	• Delete the CC-Link network parameter of the Q series CC-Link module under control of another station. • Change the setting to the head I/O number of the Q series CC-Link module under control of the host station.	Q00/Q01*12 Qn(H)*12 QnPH
		• Though the number of CC-Link module set in the network parameters is one or more, the actually mounted number of units is zero. • The head I/O number in the common parameters is different from that of the actually mounted module. • The station class for the CC-Link module quantity set parameters is different from that of the actually mounted station.	• Correct and write the network parameters. • If the error occurs after correction, it suggests a hardware fault. (Contact your local Mitsubishi representative.)	○ Rem
		The contents of the Ethernet parameter are incorrect.	Write after correcting parameters.	QnA
	3106	The CC-Link link refresh range exceeded the file register capacity.	Change the file register file for the one refresh-enabled in the whole range.	Qn(H)*12 QnPH QnPRH
		The network refresh parameter for CC-Link is out of range.	Check the parameter setting.	QCPU Rem
	3107	• The CC-Link parameter setting is incorrect. • The set mode is not be allowed for the version of the mounted CC-Link module.	Check the parameter setting.	○ Rem

*1 : Characters in parentheses ( ) indicate the special register numbers where individual information is being stored.

*12 : This applies to the CPU module of function version B or later.

Error Code (SD0)*1	Error Messages	Common Information (SD5 to 15)	Individual Information (SD16 to 26)	LED Status		Operating Statuses of CPU	Diagnostic Timing	
				RUN	ERROR			
3200	SFC PARA. ERROR	File name	Parameter number	Off	Flicker	Stop	STOP→RUN	
3201								
3202								
3203								
3300	SP. PARA ERROR	File name	Parameter number	Off	Flicker	Stop	At power ON/ At reset/ STOP→RUN	
3301							When an END instruction executed.	
							At power ON/ At reset/ STOP→RUN	
3302								
3303	SP. PARA ERROR	File name/ Drive name	Parameter number	Off	Flicker	Stop	At power ON/ At reset/ STOP→RUN	

*1 : Characters in parentheses ( ) indicate the special register numbers where individual information is being stored.

	Error Code (SD0)*1	Error Contents and Cause	Corrective Action	Corresponding CPU
	3200	The parameter setting is illegal. • Though Block 0 was set to "Automatic start" in the SFC setting of the PLC parameter dialog box, Block 0 does not exist.	Read the common information of the error using the peripheral device, check error step corresponding to its numerical value (program error location), and correct the problem.	QnA Q00J/Q00/ Q01*12 QnPH QnPRH
	3201	The block parameter setting is illegal.		QnA Qn(H) QnPH QnPRH
	3202	The number of step relays specified in the device setting of the PLC parameter dialog box is less than that used in the program.		
	3203	The execution type of the SFC program specified in the program setting of the PLC parameter dialog box is other than scan execution.		
	3300	The head I/O number in the intelligent function module parameter set on GX Configurator differs from the actual I/O number.	Check the parameter setting.	QCPU Rem
	3301	The refresh setting of the intelligent function module exceeded the file register capacity.	Change the file register file for the one which allows refresh in the whole range.	Q00J/Q00/Q01 Qn(H)*12 QnPH QnPRH
		The intelligent function module's refresh parameter setting is outside the available range.	Check the parameter setting.	QCPU Rem
	3302	The intelligent function module's refresh parameter are abnormal.	Check the parameter setting.	QCPU
	3303	In a multiple CPU system, automatic refresh setting or similar parameter setting was made to the intelligent function module under control of another station.	• Delete the automatic refresh setting or similar parameter setting of the intelligent function module under control of another CPU. • Change the setting to the automatic refresh setting or similar parameter setting of the intelligent function module under control of the host CPU.	Q00/Q01*12 Qn(H)*12 QnPH

*1 : Characters in parentheses ( ) indicate the special register numbers where individual information is being stored.

*12 : This applies to the CPU module of function version B or later.

Error Code (SD0)*1	Error Messages	Common Information (SD5 to 15)	Individual Information (SD16 to 26)	LED Status		Operating Statuses of CPU	Diagnostic Timing	
				RUN	ERROR			
3400 3401	REMOTE PASS. ERR.	----	----	Off	Flicker	Stop	At power ON/ At reset/ STOP→RUN	

*1 : Characters in parentheses ( ) indicate the special register numbers where individual information is being stored.

	Error Code (SD0)*1	Error Contents and Cause	Corrective Action	Corresponding CPU
	3400	The head I/O number of the target module in the remote password file is set to other than 0_H to $0FF0_H$.	Change the head I/O number of the target module to within the 0_H to $0FF0_H$ range.	Qn(H)*12 QnPH QnPRH Rem
		The head I/O number of the target module of the remote password is outside the following range. • Q00JCPU: 0_H to $1E0_H$ • Q00CPU/Q01CPU: 0_H to $3E0_H$	Change the head I/O number of the target module of the remote password for the number within the following range. • Q00JCPU: 0_H to $1E0_H$ • Q00CPU/Q01CPU: 0_H to $3E0_H$	Q00J/Q00/ Q01*12
	3401	Position specified as the head I/O number of the remote password file is incorrect due to one of the following reasons: • Module is not loaded. • Other than a the intelligent function module (I/O, A, QnA module) • Intelligent function module other than the Q series serial communication module, modem interface module or Ethernet module • Q series serial communication module or Ethernet module of function version A	Mount the Q series serial communication module, modem interface module or Ethernet module of function version B or later in the position specified in the head I/O No. of the remote password file.	Qn(H)*12 QnPH QnPRH Rem
		Any of the following modules is not mounted on the slot specified for the head I/O number of the remote password. • Serial communication module of function version B or later • Ethernet module of function version B or later • Modem interface module of function version B or later	Mount any of the following modules in the position specified for the head I/O number of the remote password. • Serial communication module of function version B or later • Ethernet module of function version B or later • Modem interface module of function version B or later	Q00J/Q00/ Q01*12
		The Q series serial communication module, modem interface module or Ethernet module of function version B or later controlled by another CPU was specified in a multiple CPU system.	• Change it for the Ethernet module of function version B or later connected by the host CPU. • Delete the remote password setting.	Qn(H)*12 QnPH

*1 : Characters in parentheses ( ) indicate the special register numbers where individual information is being stored.

*12 : This applies to the CPU module of function version B or later.

Error Code (SD0)*1	Error Messages	Common Information (SD5 to 15)	Individual Information (SD16 to 26)	LED Status		Operating Statuses of CPU	Diagnostic Timing	
				RUN	ERROR			
4000	INSTRCT. CODE ERR	Program error location	----	Off	Flicker	Stop	At power ON/ At reset/ STOP→RUN	
4001								
4002								
4003								
4004								
4010	MISSING END INS.	Program error location	----	Off	Flicker	Stop		
4020	CAN'T SET(P)	Program error location	----	Off	Flicker	Stop		
4021								
4030	CAN'T SET(I)	Program error location	----	Off	Flicker	Stop		

*1 : Characters in parentheses ( ) indicate the special register numbers where individual information is being stored.

	Error Code (SD0)*1	Error Contents and Cause	Corrective Action	Corresponding CPU
	4000	• The program contains an instruction code that cannot be decoded. • An unusable instruction is included in the program.	Read the common information of the error using a peripheral device, check error step corresponding to its numerical value (program error location), and correct the problem.	○
	4001	The program contains a dedicated instruction for SFC although it is not an SFC program.		QnA Q00J/Q00/Q01*12 Qn(H) QnPH QnPRH
	4002	• The extension instruction designated by the program has an incorrect instruction name. • The extension instruction specified in the program cannot be executed by the specified module.		○ Rem
	4003	The extension instruction designated by the program has an incorrect number of devices.		
	4004	The extension instruction designated by the program a device which cannot be used.		
	4010	There is no END (FEND) instruction in the program.		○
	4020	The total number of internal file pointers used by the program exceeds the number of internal file pointers set in the parameters.		QnA Qn(H) QnPH QnPRH
	4021	• The common pointer Nos. assigned to files overlap. • The local pointer Nos. assigned to files overlap.		○
	4030	The allocation pointer Nos. assigned by files overlap.		

*1 : Characters in parentheses ( ) indicate the special register numbers where individual information is being stored.

*12 : This applies to the CPU module of function version B or later.

Error Code (SD0)*1	Error Messages	Common Information (SD5 to 15)	Individual Information (SD16 to 26)	LED Status		Operating Statuses of CPU	Diagnostic Timing	
				RUN	ERROR			
4100	OPERATION ERROR	Program error location	----	Off/ On	Flicker/ On	Stop/ Continue*2	When instruction executed.	
4101								
4102								
4103								
4104								

*1 : Characters in parentheses ( ) indicate the special register numbers where individual information is being stored.

*2 : The CPU module operation status when an error occurs can be set at the parameters. (LED display will change accordingly.)

	Error Code (SD0)*1	Error Contents and Cause	Corrective Action	Corresponding CPU
	4100	The instruction cannot process the contained data.	Read the common information of the error using the peripheral device, check error step corresponding to its numerical value (program error location), and correct the problem.	○ Rem
	4101	• The designated device number for data processed by the instruction exceeds the usable range. • Alternatively, the stored data or constants for the devices designated by the instruction exceeds the usable range. • In the settings of write to the host CPU shared memory, the write designation disabled area is specified as the write destination address.		
	4102	In a multiple CPU system, the link direct device (J□\□) was specified for the network module under control of another station.	• Delete from the program the link direct device which specifies the network module under control of another CPU. • Using the link direct device, specify the network module under control of the host CPU.	Q00/Q01*12 Qn(H)*12 QnPH
		• The network No. or station No. specified for the network dedicated instruction is wrong. • The link direct device (J□\□) setting is incorrect. • The module No./network No/number of character strings specified for the extension instruction is beyond the allowed range.	Read the common information of the error using the peripheral device, check error step corresponding to its numerical value (program error location), and correct the problem.	○ Rem
	4103	The configuration of the PID dedicated instruction is incorrect.		QnA Q00J/Q00/ Q01*12 Qn(H) QnPRH
	4104	The number of settings is beyond the range.	Read the common information of the error using peripheral device, and check and correct the program corresponding to that value (program error location).	Q4AR

*1 : Characters in parentheses ( ) indicate the special register numbers where individual information is being stored.

*12 : This applies to the CPU module of function version B or later.

Error Code (SD0)*1	Error Messages	Common Information (SD5 to 15)	Individual Information (SD16 to 26)	LED Status		Operating Statuses of CPU	Diagnostic Timing	
				RUN	ERROR			
4107	OPERATION ERROR	Program error location	----	Off/ On	Flicker/ On	Stop/ Continue*2	When instruction executed.	
4108								
4109								
4111								
4112								
4120								
4121								

*1 : Characters in parentheses ( ) indicate the special register numbers where individual information is being stored.

*2 : The CPU module operation status when an error occurs can be set at the parameters. (LED display will change accordingly.)

	Error Code (SD0)*1	Error Contents and Cause	Corrective Action	Corresponding CPU
	4107	33 or more multiple CPU dedicated instructions were executed from one CPU module.	Using the multiple CPU dedicated instruction completion bit, provide interlocks to prevent one CPU module from executing 33 or more multiple CPU dedicated instructions.	Q00/Q01*12 Qn(H)*12 QnPH
		Numbers of execution to the CC-Link instruction are beyond 32.	Set the numbers of execution to the CC-Link instruction to 32 or less.	QnA
	4108	The CC-Link parameter is not set when the CC-Link instruction is executed.	Execute the CC-Link instruction after setting the CC-Link parameter.	
	4109	With high speed interrupt setting execute PR/PRC,UDCNT1, UDCONT2, PLSY, PWM,SPD,PLOADP,PUNLOADP,PAWPP instruction.	Delete the high-speed interrupt setting. When using high-speed interrupt, delete the PR, PRC, UDCNT1, UDCNT2, PLSY and PWM instructions.	Qn(H)*15
	4111	An attempt was made to perform write to the CPU shared memory write disable area of the host station CPU module with the instruction.	Read the common information of the error using GX Developer, and check and correct the error step corresponding to that value (program error location).	Q00/Q01*12
	4112	The CPU module that cannot be specified with the multiple CPU dedicated instruction was specified.		
	4120	Since the manual system switching enable flag (special register SM1592) is OFF, manual system switching cannot be executed by the control system switching instruction (SP. CONTSW).	To execute control system switching by the SP. CONTSW instruction, turn ON the manual system switching enable flag (special register SM1592).	QnPRH
	4121	• In the separate mode, the control system switching instruction (SP. CONTSW) was executed in the standby system CPU module. • In the debug mode, the control system switching instruction (SP. CONTSW) was executed.	• Reexamine the interlock signal for the SP. CONTSW instruction, and make sure that the SP. CONTSW instruction is executed in the control system only. (Since the SP. CONTSW instruction cannot be executed in the standby system, it is recommended to provide an interlock using the operation mode signal or like.) • As the SP. CONTSW instruction cannot be executed in the debug mode, reexamine the interlock signal related to the operation mode.	

*1 : Characters in parentheses ( ) indicate the special register numbers where individual information is being stored.

*12 : This applies to the CPU module of function version B or later.

*15 : This applies to the CPU module with the serial No. of which first 5 digits "04102" or later.

Error Code (SD0)*1	Error Messages	Common Information (SD5 to 15)	Individual Information (SD16 to 26)	LED Status		Operating Statuses of CPU	Diagnostic Timing	
				RUN	ERROR			
4200	FOR NEXT ERROR	Program error location	----	Off	Flicker	Stop	When instruction executed.	
4201	FOR NEXT ERROR	Program error location	----	Off	Flicker	Stop	When instruction executed.	
4202								
4203								
4210	CAN'T EXECUTE(P)	Program error location	----	Off	Flicker	Stop	When instruction executed.	
4211								
4212								
4213								
4220	CAN'T EXECUTE(I)	Program error location	----	Off	Flicker	Stop	When instruction executed.	
4221								
4223								

*1 : Characters in parentheses ( ) indicate the special register numbers where individual information is being stored.

	Error Code (SD0)*1	Error Contents and Cause	Corrective Action	Corresponding CPU
	4200	No NEXT instruction was executed following the execution of a FOR instruction. Alternatively, there are fewer NEXT instructions than FOR instructions.	Read the common information of the error using the peripheral device, check error step corresponding to its numerical value (program error location), and correct the problem.	○
	4201	A NEXT instruction was executed although no FOR instruction has been executed. Alternatively, there are more NEXT instructions than FOR instructions.		
	4202	More than 16 nesting levels are programmed.	Keep nesting levels at 16 or under.	
	4203	A BREAK instruction was executed although no FOR instruction has been executed prior to that.	Read the common information of the error using the peripheral device, check error step corresponding to its numerical value (program error location), and correct the problem.	
	4210	The CALL instruction is executed, but there is no subroutine at the specified pointer.		
	4211	There was no RET instruction in the executed subroutine program.		
	4212	The RET instruction was before the FEND instruction in the main program.		
	4213	More than 16 nesting levels are programmed.	Keep nesting levels at 16 or under.	
	4220	Though an interrupt input occurred, the corresponding interrupt pointer does not exist.	Read the common information of the error using the peripheral device, check error step corresponding to its numerical value (program error location), and correct the problem.	○
	4221	An IRET instruction does not exist in the executed interrupt program.		
	4223	An IRET instruction exists before the FEND instruction of the main program.		

*1 : Characters in parentheses ( ) indicate the special register numbers where individual information is being stored.

Error Code (SD0)*1	Error Messages	Common Information (SD5 to 15)	Individual Information (SD16 to 26)	LED Status		Operating Statuses of CPU	Diagnostic Timing	
				RUN	ERROR			
4230 4231 4235	INST. FORMAT ERR.	Program error location	----	Off	Flicker	Stop	When instruction executed.	
4300 4301	EXTEND INST. ERR.	Program error location	----	Off/ On	Flicker/ On	Stop/ Continue*2	When instruction executed.	
4400	SFCP. CODE ERROR	Program error location	----	Off	Flicker	Stop	STOP→RUN	
4410 4411	CAN'T SET(BL)	Program error location	----	Off	Flicker	Stop	STOP→RUN	
4420	CAN'T SET(S)	Program error location	----	Off	Flicker	Stop	STOP→RUN	

*1 : Characters in parentheses ( ) indicate the special register numbers where individual information is being stored.

*2 : The CPU module operation status when an error occurs can be set at the parameters. (LED display will change accordingly.)

	Error Code (SD0)*1	Error Contents and Cause	Corrective Action	Corresponding CPU
	4230	The number of CHK and CHKEND instructions is not equal.	Read the common information of the error using the peripheral device, check error step corresponding to its numerical value (program error location), and correct the problem.	QnA Qn(H) QnPH
	4231	The number of IX and IXEND instructions is not equal.		○
	4235	The configuration of the check conditions for the CHK instruction is incorrect. Alternatively, a CHK instruction has been used in a low speed execution type program.		QnA Qn(H) QnPH
	4300	The designation of a MELSECNET/ MINI-S3 master module control instruction was wrong.	Read the common information of the error using the peripheral device, check error step corresponding to its numerical value (program error location), and correct the problem.	QnA
	4301	The designation of an AD57/AD58 control instruction was wrong.		
	4400	No SFCP or SFCPEND instruction in SFC program.	Read common information of the error using the peripheral device, check error step corresponding to its numerical value (program error location), and correct the problem.	QnA Qn(H) QnPH QnPRH
	4410	The block number designated by the SFC program exceeds the range.		○
	4411	Block number designations overlap in SFC program.		
	4420	A step number designated in an SFC program exceeds the range.		

*1 : Characters in parentheses ( ) indicate the special register numbers where individual information is being stored.

*12 : This applies to the CPU module of function version B or later.

Error Code (SD0)*1	Error Messages	Common Information (SD5 to 15)	Individual Information (SD16 to 26)	LED Status		Operating Statuses of CPU	Diagnostic Timing	
				RUN	ERROR			
4421	CAN'T SET(S)	Program error location	----	Off	Flicker	Stop	STOP→RUN	
4422								
4423								
4430	SFC EXE. ERROR	File name/ Drive name	----	Off	Flicker	Stop	STOP→RUN	
4431								
4432								

*1 : Characters in parentheses ( ) indicate the special register numbers where individual information is being stored.

	Error Code (SD0)*1	Error Contents and Cause	Corrective Action	Corresponding CPU
	4421	Total number of steps in all SFC programs exceed the range.	Reduce total number of steps to below the maximum.	○
	4422	Step number designations overlap in SFC program.	Read common information of the error using the peripheral device, check error step corresponding to its numerical value (program error location), and correct the problem.	
	4423	Step relays are insufficient in the SFC program.	Read the common information of the error using GX Developer, and check and correct the error step corresponding to that value (program error location).	Q00J/Q00/ Q01*12
	4430	The SFC program cannot be executed. • The data of the block data setting is illegal. • The SFC data device of the block data setting is beyond the device setting range set in the PLC parameter.	• Write the program to the CPU module again using GX Developer. • After correcting the setting of the SFC data device, write it to the CPU module. • After correcting the device setting range set in the PLC parameter, write it to the CPU module.	
	4431	The SFC program cannot be executed. • The block parameter setting is abnormal.	Write the program to the CPU module again using GX Developer.	
	4432	The SFC program cannot be executed. • The structure of the SFC program is illegal.		

*1 : Characters in parentheses ( ) indicate the special register numbers where individual information is being stored.

*12 : This applies to the CPU module of function version B or later.

Error Code (SD0)*1	Error Messages	Common Information (SD5 to 15)	Individual Information (SD16 to 26)	LED Status		Operating Statuses of CPU	Diagnostic Timing	
				RUN	ERROR			
4500	SFCP. FORMAT ERR.	Program error location	----	Off	Flicker	Stop	STOP→RUN	
4501								
4502								
4503								
4504								
4505								
4506								
4600	SFCP. OPE. ERROR	Program error location	----	Off/ On	Flicker/ On	Stop/ Continue*2	When instruction executed.	
4601								
4602								

*1 : Characters in parentheses ( ) indicate the special register numbers where individual information is being stored.

*2 : The CPU module operation status when an error occurs can be set at the parameters. (LED display will change accordingly.)

	Error Code (SD0)*1	Error Contents and Cause	Corrective Action	Corresponding CPU
	4500	The numbers of BLOCK and BEND instructions in an SFC program are not equal.	Read common information of the error using the peripheral device, check error step corresponding to its numerical value (program error location), and correct the problem.	○
	4501	The configuration of the STEP* to TRAN* to TSET to SEND instructions in the SFC program is incorrect.		
	4502	The structure of the SFC program is illegal. • STEPI* instruction does not exist in the block of the SFC program.	Write the program to the CPU module again using GX Developer.	QnA Q00J/Q00/ Q01*12 Qn(H) QnPH QnPRH
	4503	The structure of the SFC program is illegal. • The step specified in the TSET instruction does not exist. • In jump transition, the host step number was specified as the destination step number.	• Write the program to the CPU module again using GX Developer. • Read the common information of the error using GX Developer, and check and correct the error step corresponding to that value (program error location).	
	4504	The structure of the SFC program is illegal. • The step specified in the TAND instruction does not exist.	Write the program to the CPU module again using GX Developer.	
	4505	The structure of the SFC program is illegal. • In the operation output of a step, the SET Sn/BLmSn or RST Sn/BLmSn instruction was specified for the host step.	Read the common information of the error using GX Developer, and check and correct the error step corresponding to that value (program error location).	Q00J/Q00/ Q01*12
	4506	The structure of the SFC program is illegal. • In a reset step, the host step number was specified as the destination step.		
	4600	The SFC program contains data that cannot be processed.	Read common information of the error using the peripheral device, check error step corresponding to its numerical value (program error location), and correct the problem.	QnA Qn(H) QnPH QnPRH
	4601	Exceeds device range that can be designated by the SFC program.		
	4602	The START instruction in an SFC program is preceded by an END instruction.		

*1 : Characters in parentheses ( ) indicate the special register numbers where individual information is being stored.

*12 : This applies to the CPU module of function version B or later.

Error Code (SD0)*1	Error Messages	Common Information (SD5 to 15)	Individual Information (SD16 to 26)	LED Status		Operating Statuses of CPU	Diagnostic Timing	
				RUN	ERROR			
4610 4611	SFCP. EXE. ERROR	Program error location	----	On	On	Continue	STOP→RUN	
4620 4621	BLOCK EXE. ERROR	Program error location	----	Off	Flicker	Stop	When instruction executed.	
4630 4631 4632 4633	STEP EXE. ERROR	Program error location	----	Off	Flicker	Stop	When instruction executed.	

*1 : Characters in parentheses ( ) indicate the special register numbers where individual information is being stored.

	Error Code (SD0)*1	Error Contents and Cause	Corrective Action	Corresponding CPU
	4610	The active step information at presumptive start of an SFC program is incorrect.	Read common information of the error using the peripheral device, check error step corresponding to its numerical value (program error location), and correct the problem. The program is automatically subjected to an initial start.	QnA Qn(H) QnPH QnPRH
	4611	Key-switch was reset during RUN when presumptive start was designated for SFC program.		
	4620	Startup was executed at a block in the SFC program that was already started up.	Read common information of the error using the peripheral device, check error step corresponding to its numerical value (program error location), and correct the problem.	
	4621	Startup was attempted at a block that does not exist in the SFC program.	• Read the common information of the error using GX Developer, and check and correct the error step corresponding to that value (program error location). • Turn SM321 ON if it is OFF.	QnA Q00J/Q00/ Q01*12 Qn(H) QnPH QnPRH
	4630	Startup was executed at a block in the SFC program that was already started up.	Read common information of the error using the peripheral device, check error step corresponding to its numerical value (program error location), and correct the problem.	QnA Qn(H) QnPH QnPRH
	4631	• Startup was attempted at the step that does not exist in the SFC program. Or, the step that does not exist in the SFC program was specified for end. • Forced transition was executed based on the transition condition that does not exit in the SFC program. Or, the transition condition for forced transition that does not exit in the SFC program was canceled.	• Read the common information of the error using GX Developer, and check and correct the error step corresponding to that value (program error location). • Turn SM321 ON if it is OFF.	QnA Q00J/Q00/ Q01*12 Qn(H) QnPH QnPRH
	4632	There were too many simultaneous active steps in blocks that can be designated by the SFC program.	Read common information of the error using the peripheral device, check error step corresponding to its numerical value (program error location), and correct the problem.	QnA Qn(H) QnPH QnPRH
	4633	There were too many simultaneous active steps in all blocks that can be designated.		

*1 : Characters in parentheses ( ) indicate the special register numbers where individual information is being stored.

*12 : This applies to the CPU module of function version B or later.

Error Code (SD0)*1	Error Messages	Common Information (SD5 to 15)	Individual Information (SD16 to 26)	LED Status		Operating Statuses of CPU	Diagnostic Timing	
				RUN	ERROR			
5000	WDT ERROR	Time (value set)	Time (value actually measured)	Off	Flicker	Stop	Always	
5001								
5010	PRG. TIME OVER	Time (value set)	Time (value actually measured)	On	On	Continue	Always	
5011								

*1 : Characters in parentheses ( ) indicate the special register numbers where individual information is being stored.

	Error Code (SD0)*1	Error Contents and Cause	Corrective Action	Corresponding CPU
	5000	• The scan time of the initial execution type program exceeded the initial execution watch time specified in the PLC RAS setting of the PLC parameter dialog box. • Standby system power-off occurred. • The tracking cable was disconnected and reinserted without the standby system being powered off or reset. • The tracking cable is not secured by the connector fixing screws.	• Read the individual information of the error with the peripheral device, check its value (time), and shorten the scan time. • Change the initial execution watch time or the WDT value in the PLC RAS setting of the PLC parameter dialog box. • Resolve the endless loop caused by jump transition. • Since power-off of the standby system increases the control system scan time, re-set the WDT value, taking the increase of the control system scan time into consideration. • When the tracking cable is disconnected during operation, securely connect it and restart. If the same error is displayed again, the cause is the hardware fault of the tracking cable or CPU module. (Contact your local Mitsubishi representative, explaining a detailed description of the problem.)	QnA Qn(H) QnPH QnPRH
	5001	• The program scan time exceeded the WDT value specified in the PLC RAS setting of the PLC parameter dialog box. • Standby system power-off occurred. • The tracking cable was disconnected and reinserted without the standby system being powered off or reset. • The tracking cable is not secured by the connector fixing screws.		○
	5010	• The program scan time exceeded the constant scan time specified in the PLC RAS setting of the PLC parameter dialog box. • The low speed program execution time specified in the PLC RAS setting of the PLC parameter dialog box exceeded the excess time of the constant scan.	• Review the constant scan setting time. • Review the constant scan time and low speed program execution time in the PLC parameter so that the margin time of constant scan may be fully reserved.	QnA Qn(H) QnPH QnPRH
		The program scan time exceeded the constant scan time specified in the PLC RAS setting of the PLC parameter dialog box.	• Review the constant scan time in the PLC parameter so that the margin time of constant scan may be fully reserved.	Q00J/Q00/Q01
	5011	The scan time of the low speed execution type program exceeded the low speed execution watch time specified in the PLC RAS setting of the PLC parameter dialog box.	Read the individual information of the error using the peripheral device, check the numerical value (time) there, and shorten scan time if necessary. Change the low speed execution watch time in the PLC RAS setting of the PLC parameter dialog box.	QnA Qn(H) QnPH

*1 : Characters in parentheses ( ) indicate the special register numbers where individual information is being stored.

Error Code (SD0)*1	Error Messages	Common Information (SD5 to 15)	Individual Information (SD16 to 26)	LED Status		Operating Statuses of CPU	Diagnostic Timing	
				RUN	ERROR			
	PRB. VERIFY ERR.*7	File name	----	Off	Flicker	Stop	Always	
6000	FILE DIFF.*7	File name	----	Off	Flicker	Stop	Always	

*1 : Characters in parentheses ( ) indicate the special register numbers where individual information is being stored.

*7 : This can only be detected in the redundant system standby system.

	Error Code (SD0)*1	Error Contents and Cause	Corrective Action	Corresponding CPU
	6000	The control system and standby system in the redundant system do not have the same programs and parameters.	Synchronise the programs and parameters of the control system and standby system.	Q4AR
		In a redundant system, the control system and standby system do not have the same programs and parameters. The file type detected as different between the two systems can be checked by the file name of the error common information. • The program is different. (File name = ********.QPG) • The PLC parameters/network parameters/redundant parameters are different. (File name = PARAM.QPA) • The remote password is different. (File name = PARAM.QPA) • The intelligent function module parameters are different. (File name = IPARAM.QPA) • The device initial values are different. (File name = ********.QDI) • The capacity of each write destination within the CPU for online pchange of multiple program blocks is different. (File name = MBOC.QMB)	• Match the programs and parameters of the control system and standby system. • Perform PLC verify in either of the following procedures 1), 2) to clarify the differences between the files of the two systems, then correct the wrong file, and write the corrected file to the PLC again. 1) After reading the programs/ parameters of System A using GX Developer or PX Developer, verify them with those of System B. 2) Verify the programs/parameters of GX Developer or PX Developer saved in the offline environment with those written to the CPU modules of both systems. • When the capacity of each write destination within the CPU for online change of multiple program blocks is different between the two systems, take corrective action 1) or 2). 1) Using the memory copy from control system to standby system, copy the program memory from the control system to the standby system. 2) Format the CPU module program memories of both systems. (For the capacity of each write destination within the CPU for online change of multiple program blocks, set the same value to both systems.)	QnPRH

*1 : Characters in parentheses ( ) indicate the special register numbers where individual information is being stored.

Error Code (SD0)*1	Error Messages	Common Information (SD5 to 15)	Individual Information (SD16 to 26)	LED Status		Operating Statuses of CPU	Diagnostic Timing	
				RUN	ERROR			
6001	FILE DIFF.	----	----	Off	Flicker	Stop	At power ON/ At reset/At tracking cable connection/At operation mode change	
6010	MODE. VERIFY ERR.*7	----	----	On	On	Continue	Always	
	OPE. MODE DIFF.*7	----	----	On	On	Continue	Always	
6020	OPE. MODE DIFF.*3	----	----	Off	Flicker	Stop	At power ON/ At reset	
6030	UNIT LAY. DIFF.*7	Unit No.	----	Off	Flicker	Stop	At power ON/ At reset/At tracking cable connection/At operation mode change	
6035	UNIT LAY. DIFF.*7	----	----	Off	Flicker	Stop		
6036	UNIT LAY. DIFF.*3	Unit No.	----	Off	Flicker	Stop	Always	
6040	CARD TYPE DIFF.	----	----	Off	Flicker	Stop	At power ON/ At reset	
6041		----	----	Off	Flicker	Stop	At power ON/ At reset	

*1 : Characters in parentheses ( ) indicate the special register numbers where individual information is being stored.

*3 : This can only be detected in redundant systems. Detection is possible in either the control system or the standby system.

*7 : This can only be detected in the redundant system standby system.

	Error Code (SD0)*1	Error Contents and Cause	Corrective Action	Corresponding CPU
	6001	In a redundant system, the valid parameter drive settings (SW2, SW3) made by the DIP switches are not the same.	Match the valid parameter drive settings (SW2, SW3) by the DIP switches of the control system and standby system.	QnPRH
	6010	The operational status of the control system and standby system in the redundant system is not the same.	Synchronise the operation statuses of the control system and standby system.	Q4AR
		The operational status of the control system and standby system in the redundant system is not the same.	Synchronise the operation statuses of the control system and standby system.	QnPRH
	6020	At power ON/reset, the RUN/STOP switch settings of the control system and standby system are not the same in a redundant system.	Set the RUN/STOP switches of the control system and standby system to the same setting.	
	6030	• In a redundant system, the module configuration differs between the control system and standby system. • The network module mode setting differs between the two systems.	• Match the module configurations of the control system and standby system. • In the redundant setting of the network parameter dialog box, match the mode setting of System B to that of System A.	
	6035	In a redundant system, the CPU module model name differs between the control system and standby system.	Match the model names of the control system and standby system.	
	6036	A difference in the remote I/O configuration of the MELSECNET/H multiplexed remote I/O network between the control system and standby system of a redundant system was detected.	Check the network cables of the MELSECNET/H multiplexed remote I/O network for disconnection.	
	6040	In a redundant system, the memory card installation status (installed/not installed) differs between the control system and standby system.	Match the memory card installation statuses (set/not set) of the control system and standby system.	
	6041	In a redundant system, the memory card type differs between the control system and standby system.	Match the memory card types of the control system and standby system.	

*1 : Characters in parentheses ( ) indicate the special register numbers where individual information is being stored.

Error Code (SD0)*1	Error Messages	Common Information (SD5 to 15)	Individual Information (SD16 to 26)	LED Status		Operating Statuses of CPU	Diagnostic Timing	
				RUN	ERROR			
6050	CAN'T EXE. MODE*3	----	----	On	On	Continue	Always	
6060	CPU MODE DIFF.*7	----	----	Off	Flicker	Stop	At power ON/ At reset/At tracking cable connection	
6061	CPU MODE DIFF.*7	----	----	Off	Flicker	Stop	When an END instruction executed.	
6062	CPU MODE DIFF.*8	----	----	Off	Flicker	Stop	At power ON/ At reset/At tracking cable connection	
6100	TRUCKINERR.*3	----	----	On	On	Continue	At power ON/ At reset/ STOP→RUN	
	TRK. TRANS. ERR.	Tracking transmission data classification	----	On	On	Continue	Always	

*1 : Characters in parentheses ( ) indicate the special register numbers where individual information is being stored.

*3 : This can only be detected in redundant systems. Detection is possible in either the control system or the standby system.

*7 : This can only be detected in the redundant system standby system.

*8 : Can be detected by System B of the redundant system.

	Error Code (SD0)*1	Error Contents and Cause	Corrective Action	Corresponding CPU
	6050	The function inexecutable in the debug mode or operation mode (backup/ separate mode) was executed.	Execute the function executable in the debug mode or operation mode (backup/separate mode).	QnPRH
	6060	In a redundant system, the operation mode (backup/separate) differs between the control system and standby system.	Match the operation modes of the control system and standby system.	
	6061			
	6062	Both System A and B are in the same system status (control system).	Power the CPU module (System B) which resulted in a stop error, OFF and then ON.	QnPRH
	6100	A CPU module tracking memory error was detected during initial.	Hardware fault of the CPU module. (Please contact your local nearest Mitsubishi or sales representative, explaining a detailed description of the problem. Change the CPU modules in order of the standby system CPU module and control system CPU module.)	Q4AR
		• An error (e.g. retry limit exceeded) occurred in tracking data transmission. (This error may be caused by tracking cable removal or other system power-off (including reset).) • The error occurred at a startup since the redundant system startup procedure was not followed.	• Check the CPU module or tracking cable. If the error still occurs, this indicates the CPU module or tracking cable is faulty. (Contact your local Mitsubishi representative, explaining a detailed description of the problem.) • Confirm the redundant system startup procedure, and execute a startup again.	QnPRH

*1 : Characters in parentheses ( ) indicate the special register numbers where individual information is being stored.

Error Code (SD0)*1	Error Messages	Common Information (SD5 to 15)	Individual Information (SD16 to 26)	LED Status		Operating Statuses of CPU	Diagnostic Timing	
				RUN	ERROR			
	TRUCKIN ERR.*3	----	----	On	On	Continue	When an END instruction executed.	
6101	TRK. TRANS. ERR.*3	Tracking transmission data classification	----	On	On	Continue	Always	
6102		----						
6103								
6105		Tracking transmission data classification						
6106								

*1 : Characters in parentheses ( ) indicate the special register numbers where individual information is being stored.

*3 : This can only be detected in redundant systems. Detection is possible in either the control system or the standby system.

	Error Code (SD0)*1	Error Contents and Cause	Corrective Action	Corresponding CPU
		The CPU module detected an error during the handshake for tracking.	Check the condition of the other stations.	Q4AR
	6101	• A timeout error occurred in tracking (data transmission). (This error may be caused by tracking cable removal or other system power-off (including reset).) • The error occurred at a startup since the redundant system startup procedure was not followed.	• Check the CPU module or tracking cable. If the error still occurs, this indicates the CPU module or tracking cable is faulty. (Contact your local Mitsubishi representative, explaining a detailed description of the problem.) • Confirm the redundant system startup procedure, and execute a startup again.	QnPRH
	6102	A data sum value error occurred in tracking (data reception).		
	6103	• A data error (other than sum value error) occurred in tracking (data reception). (This error may be caused by tracking cable removal or other system power-off (including reset).) • The error occurred at a startup since the redundant system startup procedure was not followed.		
	6105	• An error (e.g. retry limit exceeded) occurred in tracking (data transmission). (This error may be caused by tracking cable removal or other system power-off (including reset).) • The error occurred at a startup since the redundant system startup procedure was not followed.		
	6106	• A timeout error occurred in tracking (data transmission). (This error may be caused by tracking cable removal or other system power-off (including reset).) • The error occurred at a startup since the redundant system startup procedure was not followed.		

*1 : Characters in parentheses ( ) indicate the special register numbers where individual information is being stored.

Error Code (SD0)*1	Error Messages	Common Information (SD5 to 15)	Individual Information (SD16 to 26)	LED Status		Operating Statuses of CPU	Diagnostic Timing	
				RUN	ERROR			
6107 6108	TRK. TRANS. ERR.*3	----	----	On	On	Continue	Always	
6110	TRK. SIZE ERROR*3	Tracking capacity excess error factor	----	On	On	Continue	When an END instruction executed.	
6111	TRK. SIZE ERROR*3	----	----	On	On	Continue	When an END instruction executed.	
6112	TRK. SIZE ERROR*7	----	----	On	On	Continue	When an END instruction executed.	
6120	TRK. CABLE ERR.*3	----	----	Off	Flicker	Stop	At power ON/ At reset	

*1 : Characters in parentheses ( ) indicate the special register numbers where individual information is being stored.

*3 : This can only be detected in redundant systems. Detection is possible in either the control system or the standby system.

*7 : This can only be detected in the redundant system standby system.

	Error Code (SD0)*1	Error Contents and Cause	Corrective Action	Corresponding CPU
	6107	A data sum value error occurred in tracking (data reception).	• Check the CPU module or tracking cable. If the error still occurs, this indicates the CPU module or tracking cable is faulty. (Contact your local Mitsubishi representative, explaining a detailed description of the problem.) • Confirm the redundant system startup procedure, and execute a startup again.	QnPRH
	6108	• A data error (other than sum value error) occurred in tracking (data reception). (This error may be caused by tracking cable removal or other system power-off (including reset).) • The error occurred at a startup since the redundant system startup procedure was not followed.		
	6110	The tracking capacity exceeded the allowed range.	Reexamine the tracking capacity.	
	6111	The control system does not have enough file register capacity for the file registers specified in the tracking settings.	Switch to the file registers of which capacity is greater than the file registers specified in the tracking settings.	
	6112	File registers greater than those of the standby system were tracked and transmitted from the control system.	Switch to the file registers of which capacity is greater than the file registers specified in the tracking settings.	
	6120	• A start was made without the tracking cable being connected. • A start was made with the tracking cable faulty. • As the tracking hardware on the CPU module side was faulty, communication with the other system could not be made via the tracking cable.	Make a start after connecting the tracking cable. If the same error still occurs, this indicates the tracking cable or CPU module side tracking transmission hardware is faulty. (Contact your local Mitsubishi representative, explaining a detailed description of the problem.)	

*1 : Characters in parentheses ( ) indicate the special register numbers where individual information is being stored.

Error Code (SD0)*1	Error Messages	Common Information (SD5 to 15)	Individual Information (SD16 to 26)	LED Status		Operating Statuses of CPU	Diagnostic Timing	
				RUN	ERROR			
6130	TRK. DISCONNECT *3	----	----	On	On	Continue	Always	
6140	TRK.INIT. ERROR*3	----	----	Off	Flicker	Stop	At power ON/ At reset	

*1 : Characters in parentheses ( ) indicate the special register numbers where individual information is being stored.

*3 : This can only be detected in redundant systems. Detection is possible in either the control system or the standby system.

	Error Code (SD0)*1	Error Contents and Cause	Corrective Action	Corresponding CPU
	6130	• The tracking cable was removed. • The tracking cable became faulty while the CPU module is running. • The CPU module side tracking hardware became faulty.	• If the tracking cable was removed, connect the tracking cable to the connectors of the CPU modules of the two systems. • When the error is not resolved after connecting the tracking cable to the connectors of the CPU modules of the two systems and resetting the error, the tracking cable or CPU module side tracking hardware is faulty. (Contact your local Mitsubishi representative, explaining a detailed description of the problem.)	QnPRH
	6140	• The other system did not respond during initial communication at power ON/reset. • The error occurred at a startup since the redundant system startup procedure was not followed.	• Power the corresponding CPU module OFF and then ON again, or reset it and then unreset. If the same error still occurs, this indicates the CPU module is faulty. (Contact your local Mitsubishi representative, explaining a detailed description of the problem.) • Confirm the redundant system startup procedure, and execute a startup again.	

*1 : Characters in parentheses ( ) indicate the special register numbers where individual information is being stored.

Error Code (SD0)*1	Error Messages	Common Information (SD5 to 15)	Individual Information (SD16 to 26)	LED Status		Operating Statuses of CPU	Diagnostic Timing	
				RUN	ERROR			
6200	CONTROL EXE.*4	Reason(s) for system switching	----	On	Off	Continue	Always	
	CONTROL EXE.	Reason(s) for system switching	----	On	Off	No error	Always	
6210	CONTROL WAIT*7	Reason(s) for system switching	----	On	Off	Continue	Always	
	STANDBY	Reason(s) for system switching	----	On	Off	No error	Always	

*1 : Characters in parentheses ( ) indicate the special register numbers where individual information is being stored.

*4 : This can only be detected in the redundant system control system.

*7 : This can only be detected in the redundant system standby system.

	Error Code (SD0)*1	Error Contents and Cause	Corrective Action	Corresponding CPU
	6200	The standby system in a redundant system is switched to the control system.	Check the control system condition.	Q4AR
		The standby system has been switched to the control system in a redundant system. (Detected by the CPU that was switched from the standby system to the control system) Since this error code does not indicate the error information of the CPU module but indicates its status, the error code and error information are not stored into SD0 to 26, but are stored into the error log every system switching. (Check the error information by reading the error log using GX Developer.)	----	QnPRH
	6210	The control system in a redundant system is switched to the standby system.	Check the control system condition.	Q4AR
		The control system has been switched to the standby system in a redundant system. (Detected by the CPU that was switched from the control system to the standby system) Since this error code does not indicate the error information of the CPU module but indicates its status, the error code and error information are not stored into SD0 to 26, but are stored into the error log every system switching. (Check the error information by reading the error log using GX Developer.)	----	QnPRH

*1 : Characters in parentheses ( ) indicate the special register numbers where individual information is being stored.

Error Code (SD0)*1	Error Messages	Common Information (SD5 to 15)	Individual Information (SD16 to 26)	LED Status		Operating Statuses of CPU	Diagnostic Timing	
				RUN	ERROR			
6220	CAN'T EXE. CHANGE*4	Reason(s) for system switching	----	On	On	Continue	At switching request	
	CAN'T SWITCH	Reason(s) for system switching	Reason(s) for system switching failure	On	On	Continue	At switching execution	
6221	CAN'T EXE. CHANGE*4	Reason(s) for system switching	----	On	On	Continue	At switching request	
6230	DUAL SYS. ERROR	----	----	On	On	Continue	Always	

*1 : Characters in parentheses ( ) indicate the special register numbers where individual information is being stored.

*4 : This can only be detected in the redundant system control system.

	Error Code (SD0)*1	Error Contents and Cause	Corrective Action	Corresponding CPU
	6220	• Since the standby system is in an error or similar status in the redundant system, the control system cannot be switched to the standby system. • When an attempt was made to execute system switching, the control system could not be switched to the standby system due to a network error of the control system.	Check the standby system condition.	Q4AR
		System switching cannot be executed as the standby system or tracking cable is faulty in a redundant system. The following shows the reasons for system switching on the control system. • System switching by SP. CONTSW instruction • System switching request from network module	Check the status of the standby system and resolve the error.	QnPRH
	6221	Switching is disabled because of a bus switching module error.	This is a bus switching module hardware fault. (Contact your local Mitsubishi representative.)	Q4AR
	6230	The link module mounted on the standby type CPU is the remote master station.	Check the system configuration status.	

*1 : Characters in parentheses ( ) indicate the special register numbers where individual information is being stored.

Error Code (SD0)*1	Error Messages	Common Information (SD5 to 15)	Individual Information (SD16 to 26)	LED Status		Operating Statuses of CPU	Diagnostic Timing	
				RUN	ERROR			
6300	STANDBY SYS. DOWN*4	----	----	On	On	Continue	Always	
6310	CONTROL SYS. DOWN*7	----	----	Off	Flicker	Stop	Always	

*1 : Characters in parentheses ( ) indicate the special register numbers where individual information is being stored.

*4 : This can only be detected in the redundant system control system.

*7 : This can only be detected in the redundant system standby system.

	Error Code (SD0)*1	Error Contents and Cause	Corrective Action	Corresponding CPU
	6300	Any of the following errors was detected in the backup mode. • The standby system has not started up in the redundant system. • The standby system has developed a stop error in the redundant system. • The CPU module in the debug mode was connected to the operating control system.	• Check whether the standby system is on or not, and if it is not on, power it on. • Check whether the standby system has been reset or not, and if it has been reset, unreset it. • Check whether the standby system has developed a stop error or not, and if it has developed the error, remove the error factor and restart it. • When the CPU module in the debug mode was connected to the control system operating in the backup mode, make connection so that the control system and standby system are combined correctly.	QnPRH
	6310	Any of the following errors was detected in the backup mode. • The control system has not started up in the redundant system. • The control system has developed a stop error in the redundant system. • The CPU module in the debug mode was connected to the operating standby system. • The error occurred at a startup since the redundant system startup procedure was not followed.	• The standby system exists but the control system does not exist. • Check whether the system other than the standby system is on or not, and if it is not on, power it on. • Check whether the system other than the standby system has been reset or not, and if it is has been reset, unreset it. • Check whether the system other than the standby system has developed a stop error or not, and if has developed the error, remove the error factor, set the control system and standby system to the same operating status, and restart. • When the CPU module in the debug mode was connected to the control system operating in the backup mode, make connection so that the control system and control system are combined correctly. • Confirm the redundant system startup procedure, and execute a startup again.	QnPRH

*1 : Characters in parentheses ( ) indicate the special register numbers where individual information is being stored.

Error Code (SD0)*1	Error Messages	Common Information (SD5 to 15)	Individual Information (SD16 to 26)	LED Status		Operating Statuses of CPU	Diagnostic Timing	
				RUN	ERROR			
6311	CONTROL SYS. DOWN*7	----	----	Off	Flicker	Stop	At power ON/ At reset	
6312								
6400	PRG. MEM. CLEAR	----	----	Off	Flicker	Stop	At execution of the memory copy from control system to standby system	
6410	MEM.COPY EXE*4	----	----	On	On	Continue	At execution of the function of copying memory from control system to standby system	
6500	TRK. PARA. ERROR	File name/ Drive name	Parameter number	Off	Flicker	Stop	At power ON/ At reset	
6501		File name/ Drive name	Parameter number	Off	Flicker	Stop	At power ON/ At reset	
7000	MULTI CPU DOWN	Unit No.	----	Off	Flicker	Stop	Always	
							At power ON/ At reset	

*1 : Characters in parentheses ( ) indicate the special register numbers where individual information is being stored.

*4 : This can only be detected in the redundant system control system.

*7 : This can only be detected in the redundant system standby system.

	Error Code (SD0)*1	Error Contents and Cause	Corrective Action	Corresponding CPU
	6311 6312	• As consistency check data has not transmitted from the control system in a redundant system, the other system cannot start as a standby system. • The error occurred at a startup since	• Replace the tracking cable. If the same error still occurs, this indicates the CPU module is faulty. (Contact your local Mitsubishi representative, explaining a detailed description of	QnPRH
	6400	The memory copy from control system to standby system was executed, and the program memory was cleared.	After the memory copy from control system to standby system is completed, switch power OFF and then ON, or make a reset.	QnPRH
	6410	The memory copy from control system to standby system was executed.	----	
	6500	The file register file specified in the tracking setting of the PLC parameter dialog box does not exist.	Read the individual information of the error using GX Developer, and check and correct the drive name and file name. Create the specified file.	QnPRH
	6501	The file register range specified in the device detail setting of the tracking setting of the PLC parameter dialog box exceeded the specified file register file capacity.	Read the individual information of the error using GX Developer, and increase the file register capacity.	
	7000	• In a multiple CPU system, at CPU module fault occurred at a station where "All station stop by stop error of PLC " was selected in the operating mode. • In a multiple CPU system, a CPU module incompatible with the multiple CPU system was mounted.	• Read the individual information of the error using the GX Developer, check the error of the PLC resulting in CPU module fault, and remove the error. • Remove the CPU module incompatible with the multiple CPU system from the main base unit.	Q00/Q01*12 Qn(H)*12 QnPH
		In a multiple CPU system, station 1 resulted in stop error at power-on and the other stations cannot start. (This error occurred at stations 2 to 4)	Read the individual information of the error using the GX Developer, check the error of the CPU module resulting in CPU module fault, and remove the error.	

*1 : Characters in parentheses ( ) indicate the special register numbers where individual information is being stored.

*12 : This applies to the CPU module of function version B or later.

Error Code (SD0)*1	Error Messages	Common Information (SD5 to 15)	Individual Information (SD16 to 26)	LED Status		Operating Statuses of CPU	Diagnostic Timing	
				RUN	ERROR			
7002 7003	MULTI CPU DOWN	Unit No.	----	Off	Flicker	Stop	At power ON/ At reset	
7004	MULTI CPU DOWN	Unit No.	----	Off	Flicker	Stop	Always	
7010	MULTI EXE. ERROR	Unit No.	----	Off	Flicker	Stop	At power ON/ At reset	

*1 : Characters in parentheses ( ) indicate the special register numbers where individual information is being stored.

	Error Code (SD0)*1	Error Contents and Cause	Corrective Action	Corresponding CPU
	7002	• There is no response from the target CPU module in a multiple CPU system at initial communication stage. • In a multiple CPU system, a CPU module incompatible with the multiple CPU system was mounted.	• Reset the CPU module and RUN it again. If the same error is displayed again, this suggests the hardware fault of any of the CPU modules. (Contact your local Mitsubishi representative.) • Remove the CPU module incompatible with the multiple CPU system from the main base unit. Or, replace the CPU module incompatible with the multiple CPU system with the compatible one.	Q00/Q01*12 Qn(H)*12 QnPH
	7003	There is no response from the target CPU module in a multiple CPU system at initial communication stage.	Reset the CPU module and RUN it again. If the same error is displayed again, this suggests the hardware fault of any of the CPU modules. (Contact your local Mitsubishi representative.)	
	7004	In a multiple CPU system, a data error occurred in communication between the CPU modules.	• Check the system configuration to see if modules are mounted in excess of the number of I/O points. • When there are no problems in the system configuration, this indicates the CPU module hardware is faulty. (Contact your local Mitsubishi representative, explaining a detailed description of the problem.)	Q00/Q01*12
	7010	• In a multiple CPU system, a faulty CPU module was mounted. • In a multiple CPU system, a CPU module incompatible with the multiple CPU system was mounted. (The CPU module compatible with the multiple CPU system was used to detect an error.) • In a multiple CPU system, any of the CPU No. 2 to 4 was reset with power ON. (The unreset CPU was used to detect an error.) • In a multiple CPU system, the PC CPU module used the QFB (bus interface driver) of version 1.06 or earlier.	• Read the individual information of the error using GX Developer, and replace the faulty CPU module. • Replace the CPU module incompatible with the multiple CPU system with the one compatible with the multiple CPU system. • Do not reset any of the No. 2 to 4 CPU modules. • Use the PC CPU module that uses the QFB of version 1.07 or later. • Reset the No. 1 CPU module and restart up the multiple CPU system.	Q00/Q01*12 Qn(H)*12 QnPH

*1 : Characters in parentheses ( ) indicate the special register numbers where individual information is being stored.

*12 : This applies to the CPU module of function version B or later.

Error Code (SD0)*1	Error Messages	Common Information (SD5 to 15)	Individual Information (SD16 to 26)	LED Status		Operating Statuses of CPU	Diagnostic Timing	
				RUN	ERROR			
7011	MULTI EXE. ERROR	Unit No.	----	Off	Flicker	Stop	At power ON/ At reset	
7020	MULTI CPU ERROR	Unit No.	----	On	On	Continue	Always	
7030	CPU LAY. ERROR	Unit No.	----	Off	Flicker	Stop	At power ON/ At reset	
7031								
7032								
7035	CPU LAY. ERROR	Unit No.	----	Off	Flicker	Stop	At power ON/ At reset	

*1 : Characters in parentheses ( ) indicate the special register numbers where individual information is being stored.

	Error Code (SD0)*1	Error Contents and Cause	Corrective Action	Corresponding CPU
	7011	Either of the following settings was made in a multiple CPU system. • Multiple CPU automatic refresh setting was made for the inapplicable CPU module. • "I/O sharing when using multiple CPUs" setting was made for the inapplicable CPU module.	• Correct the multiple CPU automatic refresh setting. • Correct the "I/O sharing when using multiple CPUs" setting.	Q00/Q01*12
	7020	In a multiple CPU system, an error occurred in the CPU "where system stop was not selected" in the operation mode. (The CPU module where no error occurred was used to detect an error.)	Read the individual information of the error using the peripheral device, check the error of the CPU module resulting in CPU module fault, and remove the error.	Q00/Q01*12 Qn(H)*12 QnPH
	7030	An assignment error occurred in the CPU-mountable slot (CPU slot, I/O slot 0, 1) in excess of the number of CPU modules specified in the multiple CPU setting of the PLC parameter dialog box.	• Set the same value to the number of CPU modules specified in the multiple CPU setting of the PLC parameter dialog box and the number of mounted CPU modules (including CPU (empty)). • Make the type specified in the I/O assignment setting of the PLC parameter dialog box consistent with the CPU module configuration.	Q00/Q01*12
	7031	An assignment error occurred within the range of the number of CPUs specified in the multiple CPU setting of the PLC parameter dialog box.		
	7032	• The number of CPU modules mounted in a multiple CPU system is wrong. (Q00CPU or Q01CPU: more than 1 module, motion CPU: more than 1 module, PC CPU module: more than 1 module) • The PLC CPU is mounted as the CPU No. 2 or later. • The motion CPU is mounted as the CPU No. 3.	Configure a multiple CPU system so that the following restriction will be satisfied: Q00CPU or Q01CPU: within 1 module the motion CPU: within 1 module PC CPU module: within 1 module.	
	7035	The CPU module has been mounted on the inapplicable slot.	Mount the CPU module on the applicable slot.	Q00J/Q00/Q01*12 QnPRH

*1 : Characters in parentheses ( ) indicate the special register numbers where individual information is being stored.

*12 : This applies to the CPU module of function version B or later.

Error Code (SD0)*1	Error Messages	Common Information (SD5 to 15)	Individual Information (SD16 to 26)	LED Status		Operating Statuses of CPU	Diagnostic Timing	
				RUN	ERROR			
9000	F**** *9	Program error location	Annunciator number	On	On/ Off *11	Continue	When instruction executed.	
				USER LED On				
9010	<CHK>ERR ***-*** *10	Program error location	Failure No.	On	Off	Continue	When instruction executed.	
				USER LED On				
9020	BOOT OK	----	----	Off	Flicker	Stop	At power ON/ At reset	
10000	CONT. UNIT ERROR	----	----	Off	Flicker	Continue	Always	

*1 : Characters in parentheses ( ) indicate the special register numbers where individual information is being stored.

*9 : **** indicates the detected annunciator No.

*10 : ***_*** indicates the detected contact and coil No.

*11 : For the Basic model QCPU, it can be turned off by the LED indication priority setting special register (SD207 to SD209). (Only turned off for the High Performance model QCPU, Process CPU or Redundant CPU.)

	Error Code (SD0)*1	Error Contents and Cause	Corrective Action	Corresponding CPU
	9000	Annunciator (F) was set ON	Read the individual information of the error using the peripheral device, and check the program corresponding to the numerical value (annunciator number).	○
	9010	Error detected by the CHK instruction.	Read the individual information of the error using the peripheral device, and check the program corresponding to the numerical value (error number) there.	QnA Qn(H) QnPH QnPRH
	9020	Storage of data onto ROM was completed normally in automatic write to standard ROM. (BOOT LED also flickers.)	Use the DIP switches to set the valid parameter drive to the standard ROM. Then, switch power on again, and perform boot operation from the standard ROM.	Qn(H)*12 QnPH QnPRH
	10000	In the multiple CPU system, an error occurred in the CPU module other than the Process CPU/High Performance model QCPU.	Check the details of the generated error by connecting to the corresponding CPU module using GX Developer.	Qn(H)*12 QnPH

*1 : Characters in parentheses ( ) indicate the special register numbers where individual information is being stored.

*12 : This applies to the CPU module of function version B or later.

6. TRANSPORTATION PRECAUTIONS

When transporting lithium batteries, make sure to treat them based on the transport regulations.

6.1 Controlled Models

The batteries for the MELSEC-QnA series CPU module (including memory cards) are classified as follows:

Product name	Model	Product supply status	Classification for transportation
QnA series battery	A6BAT	Lithium battery	Non-dangerous goods
QnA series memory card	Q1MEM-128S, Q1MEM-128SE, Q1MEM-1MS, Q1MEM-1MSE, Q1MEM-1MSF, Q1MEM-256S, Q1MEM-256SE, Q1MEM-256SF, Q1MEM-2MS, Q1MEM-2MSF, Q1MEM-512S, Q1MEM-512SE, Q1MEM-512SF, Q1MEM-64S, Q1MEM-64SE	Packed with lithium coin battery (BR2325)	

6.2 Transport Guidelines

Comply with IATA Dangerous Goods Regulations, IMDG code and the local transport regulations when transporting products after unpacking or repacking, while Mitsubishi ships products with packages to comply with the transport regulations.
Also, contact the transporters.

Warranty

Mitsubishi will not be held liable for damage caused by factors found not to be the cause of Mitsubishi; machine damage or lost profits caused by faults in the Mitsubishi products; damage, secondary damage, accident compensation caused by special factors unpredictable by Mitsubishi; damages to products other than Mitsubishi products; and to other duties.

⚠For safe use

- This product has been manufactured as a general-purpose part for general industries, and has not been designed or manufactured to be incorporated in a device or system used in purposes related to human life.
- Before using the product for special purposes such as nuclear power, electric power, aerospace, medicine or passenger movement vehicles, consult with Mitsubishi.
- This product has been manufactured under strict quality control. However, when installing the product where major accidents or losses could occur if the product fails, install appropriate backup or failsafe functions in the system.

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